

NC STATE

DrCCTProf: Supporting Fine-Grained Call Path Profiling on ARM and X86

Xu Liu

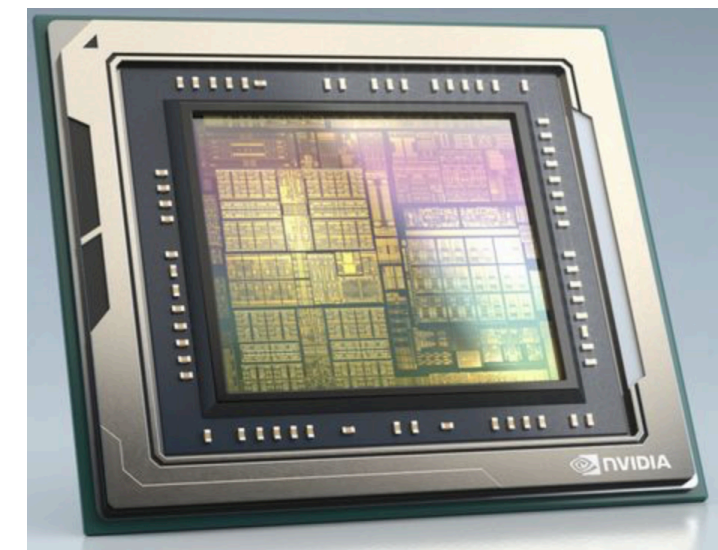
NC State University

ARM is Popular

ARM for Smartphones



ARM for SoC



ARM for PC (laptop, tablet)

ARM for High Performance Computing

ARM Cloud Platform



Graviton
processors

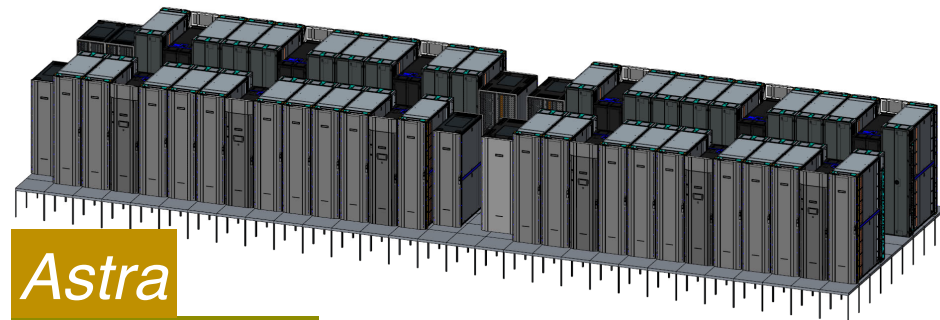


HUAWEI
Kunpeng
processors



Marvell ThunderX2
processors

ARM Supercomputer



Astra

Aug 2018

Consists of 145152 ARM processors



Fugaku

June 2020

No.1 Supercomputer



Tianhe-3E

Before 2021

The exascale supercomputer

ARM Ecosystem

Example End Users



Sandia
National
Laboratories



Hartree Centre
Science & Technology Facilities Council



PayPal

ARM®

Key Applications Middleware

OpenJDK

MySQL®



APACHE
HTTP SERVER



NGINX



MariaDB



Couchbase



ceph

Operating System, Virtualization & Firmware



ACPI



fedora



redhat

ubuntu
Supported by Canonical



debian

OEMs and ODMs

CRAY
THE SUPERCOMPUTER COMPANY
MITAC
MITAC INTERNATIONAL CORP.



FOXCONN®
Inventec

STACK
VELOCITY

GIGABYTE™
wiwynn



hyve
solutions

PEGATRON
ASUS®

ARM SoC

AMD



apm
applied
micro



CAVIUM



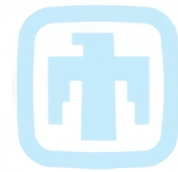
QUALCOMM®



TEXAS
INSTRUMENTS

ARM Ecosystem

Example End Users



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PayPal

ARM®

Key Applications

MySQL®



MariaDB



Couchbase



Demand performance tools on various systems

fedora®



redhat.

UBUNTU®
Supported by Canonical

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CAVIUM



QUALCOMM®



TEXAS
INSTRUMENTS

Mainstream Profilers

Popular profilers

MAP / Perf / TAU / Scalasca / HPCToolkit / VTune / ...

- Hotspot analysis: high resource utilization
- Various hardware metrics: cache misses, instructions per cycle, ...

However,

They cannot tell if resources were **“well spent”**

Hotspots may be symptoms rather than root causes

A Motivating Example

```
1 for ( i = T; i < N - T; i++) {  
2   for ( j = T; j < M - T; j++){  
3     temp = 0;  
4     for ( k = 1; k < T; k++)  
5       temp += matrix[i-k][j] + matrix[i][j-k] +  
6           matrix[i+k][j] + matrix[i][j+k];  
7   matrix[i][j] += temp;  
8 }  
9 }
```

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Many redundant computations

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Many redundant computations

metrics	original	optimized	&redunction
#instructions	73*10 ⁹	40*10 ⁹	0.46
#cycles	33*10 ⁹	21*10 ⁹	0.36
IPC	2.2	1.9	

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#instructions	73*10 ⁹	40*10 ⁹	0.46

Need techniques to shift from hotspot analysis to

wastage analysis

IPC	2.2	1.9	
-----	-----	-----	--

Wastage Analysis

Wasted memory accesses

Redundant memory accesses

- Redundant memory accesses: the same values involved

Useless memory accesses

- Dead stores: stored value got overwritten without use

Wasted arithmetic computation

Symbolic equivalent computation

Result equivalent computation

HMMER: An Example for Resource Wastage

Unoptimized

```
for (i = 1; i <= L; i++) {  
  for (k = 1; k <= M; k++) {  
    mc[k] = mpp[k-1] + tpmm[k-1];  
    if ((sc = ip[k-1] + tpim[k-1]) > mc[k])  
      mc[k] = sc;
```

-O3 optimized

```
for (i = 1; i <= L; i++) {  
  for (k = 1; k <= M; k++) {  
    R1= mpp[k-1] + tpmm[k-1];  
    mc[k] = R1;  
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  else
    mc[k] = R1;
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Never Alias.
Declare as “restrict” pointers.
Can vectorize.

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> 16% running time improvement
> 40% with vectorization

HMMER: From the Binary Perspective

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```

```

1  mov %r10,%rax,4),%ecx
2  add 0x0(%r13,%rax,4),%ecx #mpp[k-1]+tpmm[k-1]
3  mov %ecx,0x4(%rdx)      #assign mc[k]
4  mov 0x18(%rsp),%rbx
5  mov (%r9,%rax,4),%r15d
6  add (%rbx,%rax,4),%r15d #dpp[k-1]+tpdm[k-1]
7  mov 0x20(%rsp),%rbx
8  cmp %ecx,%r15d          #%ecx is mc[k]
9  cmovge %r15d,%ecx
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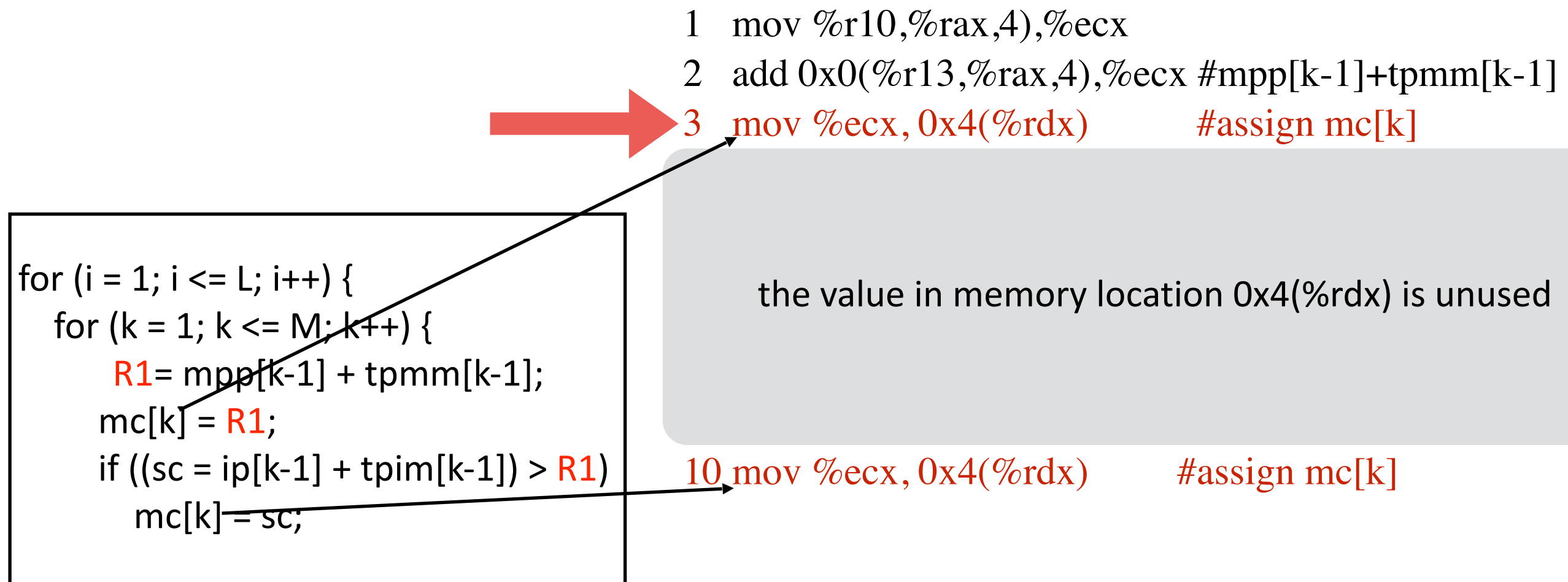
the value in memory location 0x4(%rdx) is unused

```

10 mov %ecx,0x4(%rdx)      #assign mc[k]

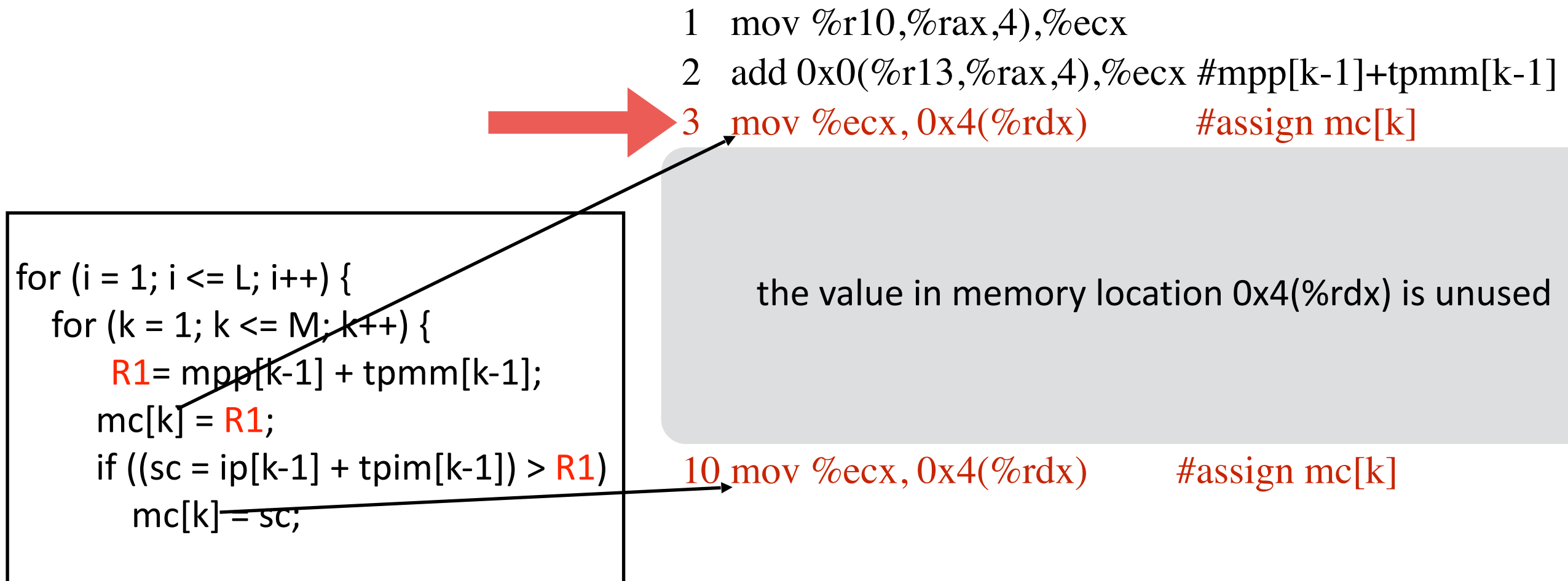
```

HMMER: From the Binary Perspective



dead store

HMMER: From the Binary Perspective



Dynamic fine-grained binary analysis
is necessary

Dynamic Fine-Grained Binary Tools

Microscopic insights

Track each instruction

- Operators and operands

Track each register

- General and SIMD registers

Track each memory location

- Effective addresses

Track values in storage locations

- Values in registers and memory

Existing Dynamic Fine-Grained Binary Tools

Tools

Valgrind / **DynamoRIO** / Dyninst / Pin

- High overhead but **microscopic insights**
- Suitable for analyzing software performance/correctness bugs

However,

Difficult to master

- Many complex APIs

Significant engineering efforts

- Obtaining deep insights
- Reducing measurement overhead

Existing Dynamic Fine-Grained Binary Tools

Tools

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DrCCTProf facilitates the dynamic fine-grained binary analysis
with easy interfaces

DrCCTProf Highlights

Rich insights

Fine-grained **call path** profiling

Overhead optimization

Handling efficient instrumentation

Handling parallelism

Easy interfaces

Easy instrumentation

Easy analysis

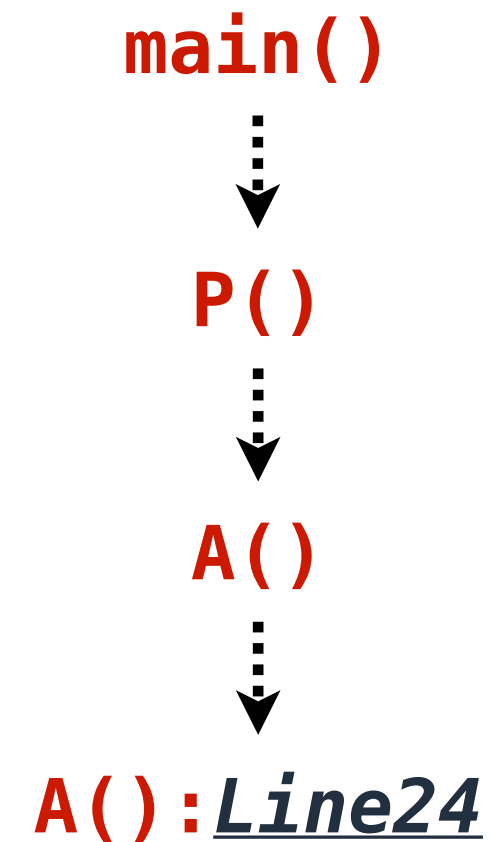
Easy visualization

What is a Calling Context ?

```
1:  void main(){
2:      P();
3:  }
4:  void P(){
5:      A();
6:      B();
7:      C();
8:      D();
...      .....
21:     A();
22: }
23: void A(){
24:     *ptr = 100;
25:     x++;
26:     return;
27: }
```

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What is a Calling Context?

Performance Analysis Tools			
155	div(m,n) = div(m,n) - (elem%spheremp(j,n)*vtemp(j,n,1)*deriv%Dvv(m,j		
156	elem%spheremp(m,j)*vtemp(m,j,2)*deriv%Dvv(n,j		
157	enddo		
<div> <div>Top-down view</div> <div>Bottom-up view</div> <div>Flat view</div> </div>			
<div> <div>↑ ↓ 🔥 f(x) 📊 CSV A+ A-] 🔄</div> </div>			
Scope			
▼ 196: [I] laplace_sphere_wk		1.45e+07	96.5%
▼ 181: [I] divergence_sphere_wk		1.45e+07	96.5%
▼ loop at biharmonic_wk_kernel.F90: 151		1.45e+07	96.5%
▼ loop at biharmonic_wk_kernel.F90: 154		1.45e+07	96.5%

Debuggers	
Call Stack	
Name	Lang
ConsoleApplication1.exe!writeFileChunk::_J2::<lambda>() Line 267	C++
ConsoleApplication1.exe!std::_Callable_obj<bool <lambda>(void),0>::_ApplyX<bool>() Line 284	C++
ConsoleApplication1.exe!std::_Func_impl<std::_Callable_obj<bool <lambda>(void),0>,std::allocator<std::_Func_cla	C++
ConsoleApplication1.exe!std::_Func_class<bool>::operator()() Line 315	C++
ConsoleApplication1.exe!Concurrency::task<bool>::_InitialTaskHandle<bool,bool <lambda>(void),Concurrency::de	C++
ConsoleApplication1.exe!Concurrency::task<bool>::_InitialTaskHandle<bool,bool <lambda>(void),Concurrency::de	C++
ConsoleApplication1.exe!Concurrency::task<bool>::_InitialTaskHandle<bool,bool <lambda>(void),Concurrency::de	C++
ConsoleApplication1.exe!Concurrency::details::_PPLTaskHandle<bool,Concurrency::task<bool>::_InitialTaskHandle<	C++
ConsoleApplication1.exe!Concurrency::details::_TaskProcHandle::operator()() Line 110	C++
ConsoleApplication1.exe!Concurrency::details::_UnrealizedChore::_InvokeBridge<Concurrency::details::_TaskProcHar	C++
msvcr120d.dll!Concurrency::details::_UnrealizedChore::_UnstructuredChoreWrapper(Concurrency::details::_Unrealize	C++
msvcr120d.dll!Concurrency::details::_UnrealizedChore::_Invoke() Line 4333	C++
msvcr120d.dll!Concurrency::details::WorkItem::_Invoke() Line 172	C++

Chain of function calls that led to the current point in the program.
(a.k.a **Call Path / Call Stack / Backtrace / Activation Record**)

Why Calling Contexts are Necessary?

```
movsdq 0x8(%rdi,%r10,8), %xmm0:__mul:<no src>
```

SPEC 2006: bwaves

A pair of redundant computation

```
*****REDUNDANT WITH *****  
movsdq 0x8(%rdi,%r10,8), %xmm0:__mul:<no src>
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SPEC 2006: bwaves

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```
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```
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```

```
__ieee754_pow_sse2: <no src>
```

```
pow: <no src>
```

```
jacobian_: jacobian_lam.f:47
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shell_: shell_lam.f:193
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MAIN__: flow_lam.f:63
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41. ros = q(1, ip1, jp1, kp1)
42. us = q(2, ip1, jp1, kp1)/ros
    ...
47. mu = (mu +
((gm-1.0d0)*(q(5,ip1,jp1,kp1)/ros-
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No insights without call path
profiling

A pair of redundant computation

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__dvd: <no src>
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```
__mpexp: <no src>
```

```
__mplog: <no src>
```

```
__slowpow: <no src>
```

```
__ieee754_pow_sse2: <no src>
```

```
pow: <no src>
```

```
jacobian_:jacobian_lam.f:47
```

```
shell_:shell_lam.f:193
```

```
MAIN__:flow_lam.f:63
```

```
main:flow_lam.f:67
```

```
*****REDUNDANT WITH *****
```

```
movsdq 0x8(%rdi,%r10,8), %xmm0: __mul:<no src>
```

```
__dvd: <no src>
```

```
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```

```
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```
MAIN__:flow_lam.f:63
```

```
main:flow_lam.f:67
```

DrCCTProf Overview

Monitor unmodified, fully-optimized binary executables on ARM.

Ubiquitous call path collection **[code-centric]**

- Associate **calling context** with their metrics potentially on **every executed machine instruction**

Ubiquitous object attribution **[data-centric]**

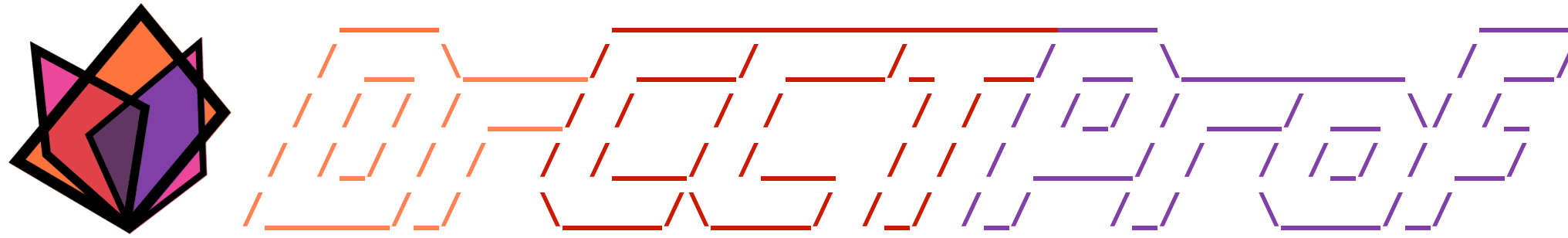
- Associate **every memory address** with the **corresponding data object**.

Programmability

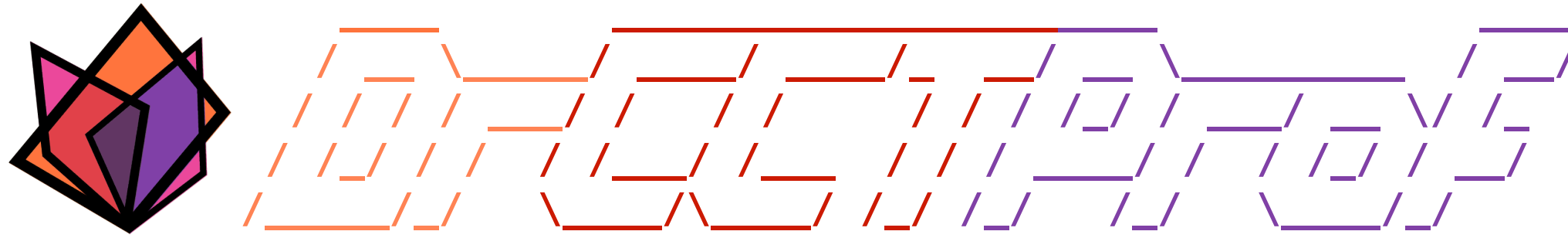
Framework to support various tools

Affordable overhead

Suitable for large scale execution



- Ubiquitous call path collection
- Attributing costs to data objects
- Handling parallelism
- Evaluation
- Case study
- Conclusions

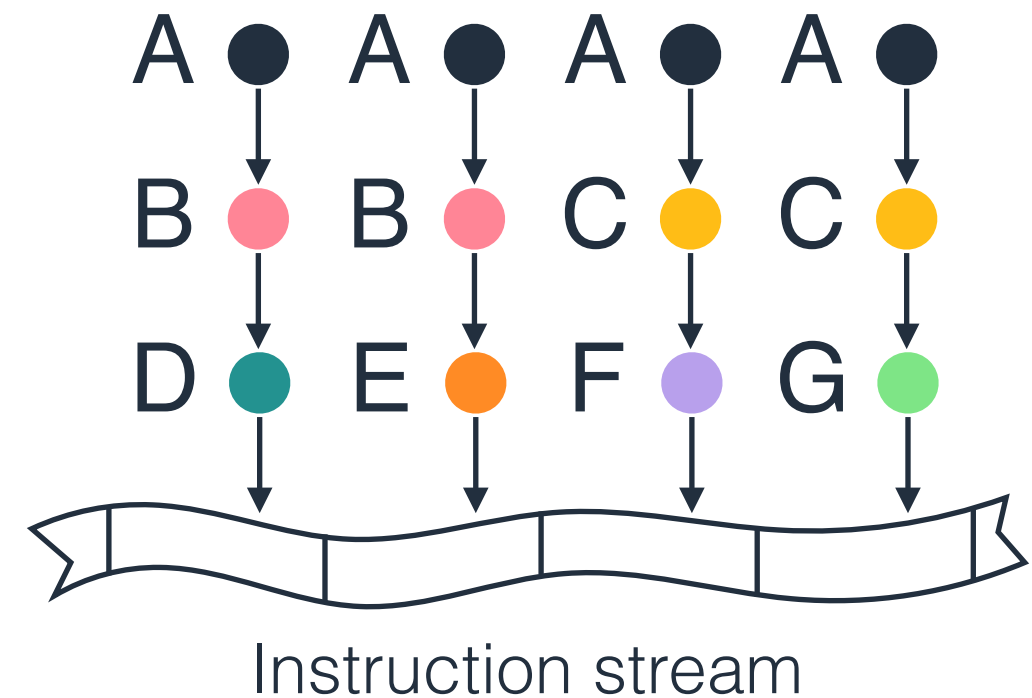


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- Attributing costs to data objects
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Store History of Contexts Compactly

Problem

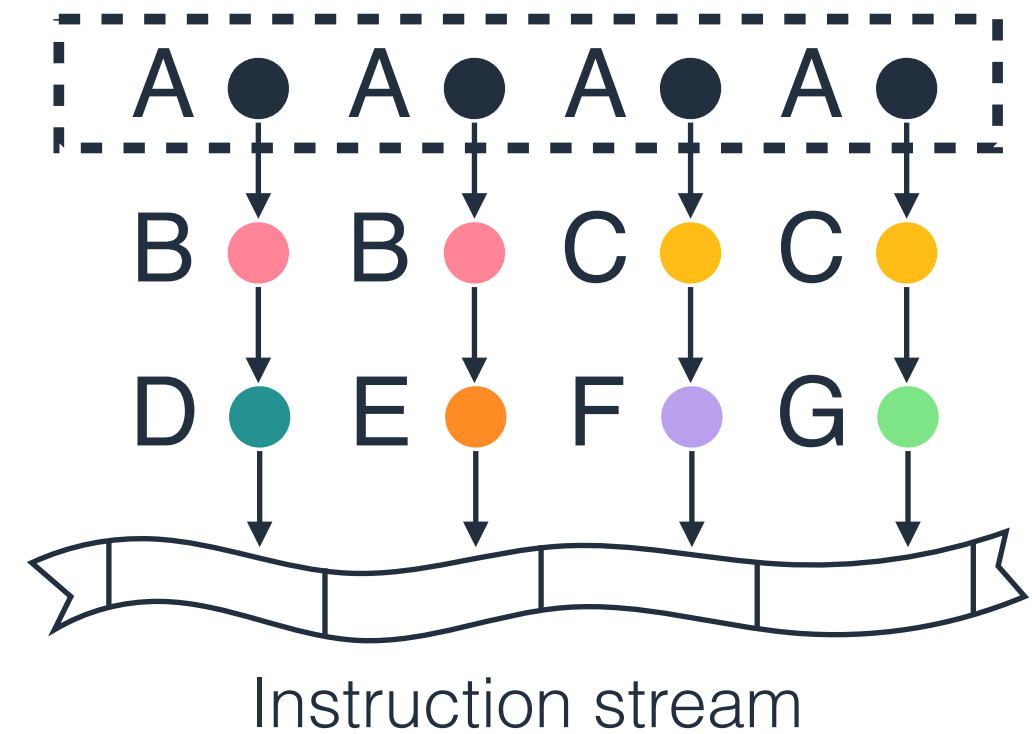
Deluge of call paths



Store History of Contexts Compactly

Problem

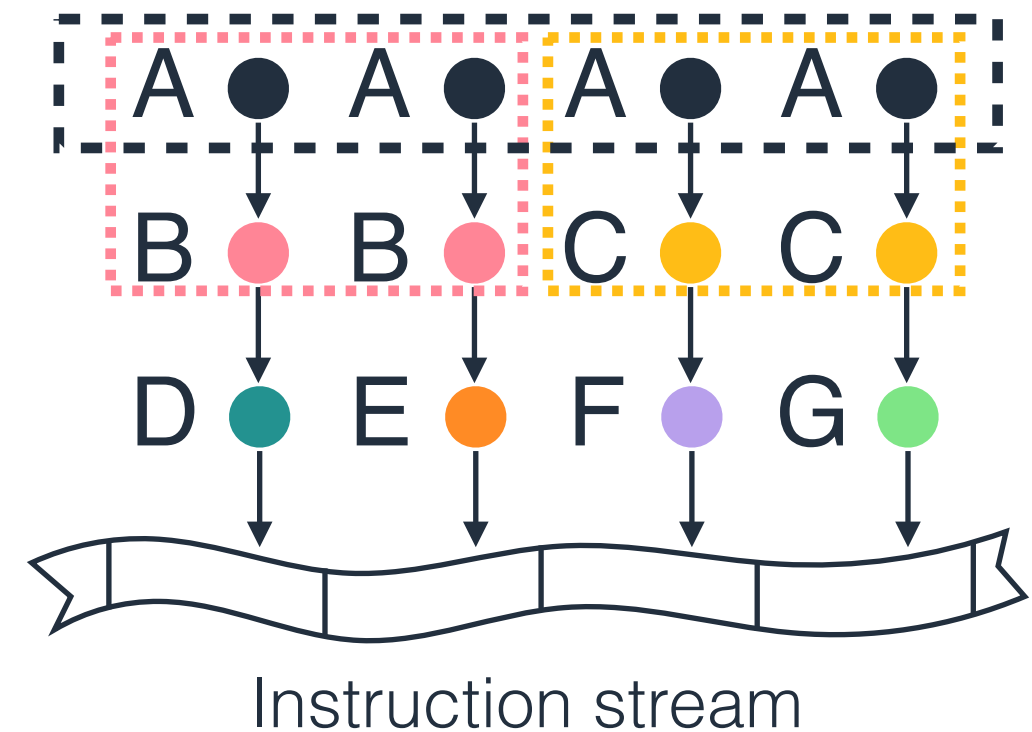
Deluge of call paths



Store History of Contexts Compactly

Problem

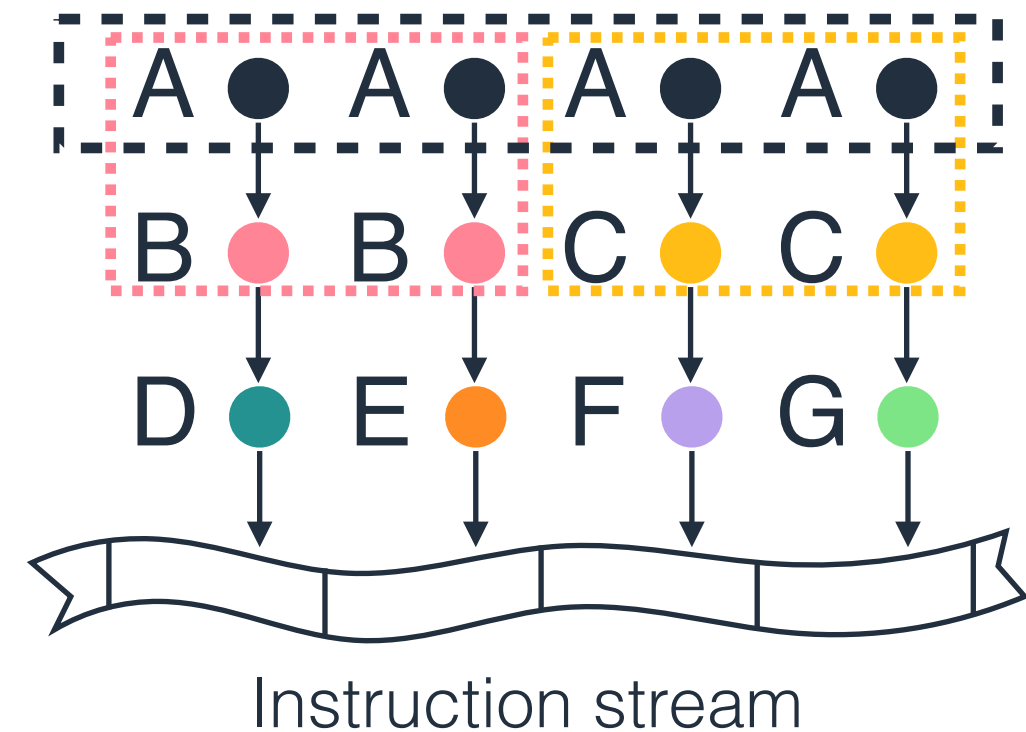
Deluge of call paths



Store History of Contexts Compactly

Solution

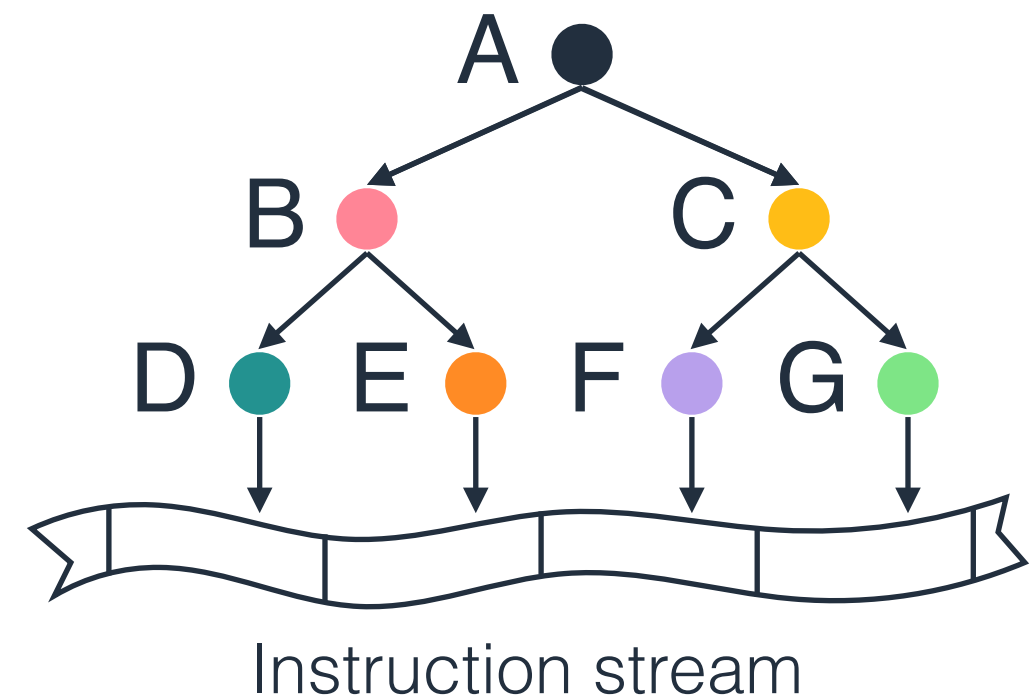
- ✓ Call paths share common prefix
- ✓ Store call paths as a calling context tree (CCT)
- ✓ One CCT per thread



Store History of Contexts Compactly

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- ✓ Call paths share common prefix
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- ✓ One CCT per thread



Shadowing Call Stack

Solution *Reverse the process. Eagerly build a replica/shadow stack on-the-fly.*

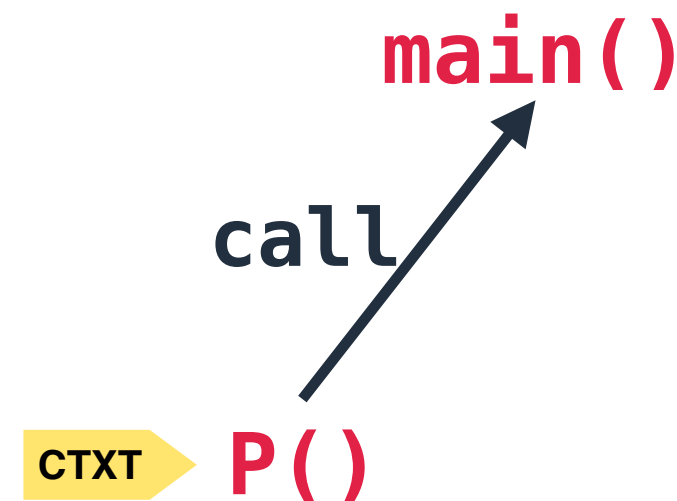
```
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2:      P();
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7:      C();
8:      D();
...
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22:  }
23:  void A(){
24:      *ptr = 100;
25:      x++;
26:      return;
27:  }
```

main()

Shadowing Call Stack

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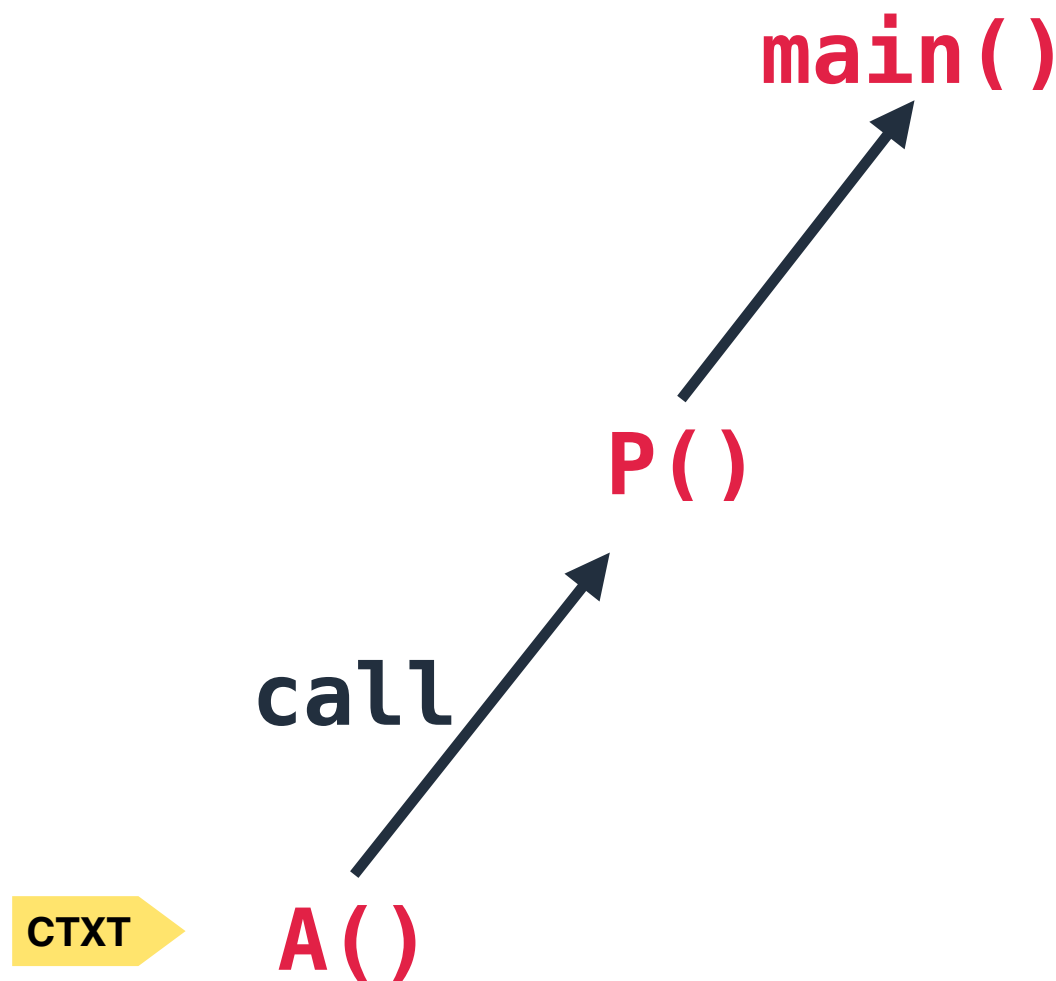
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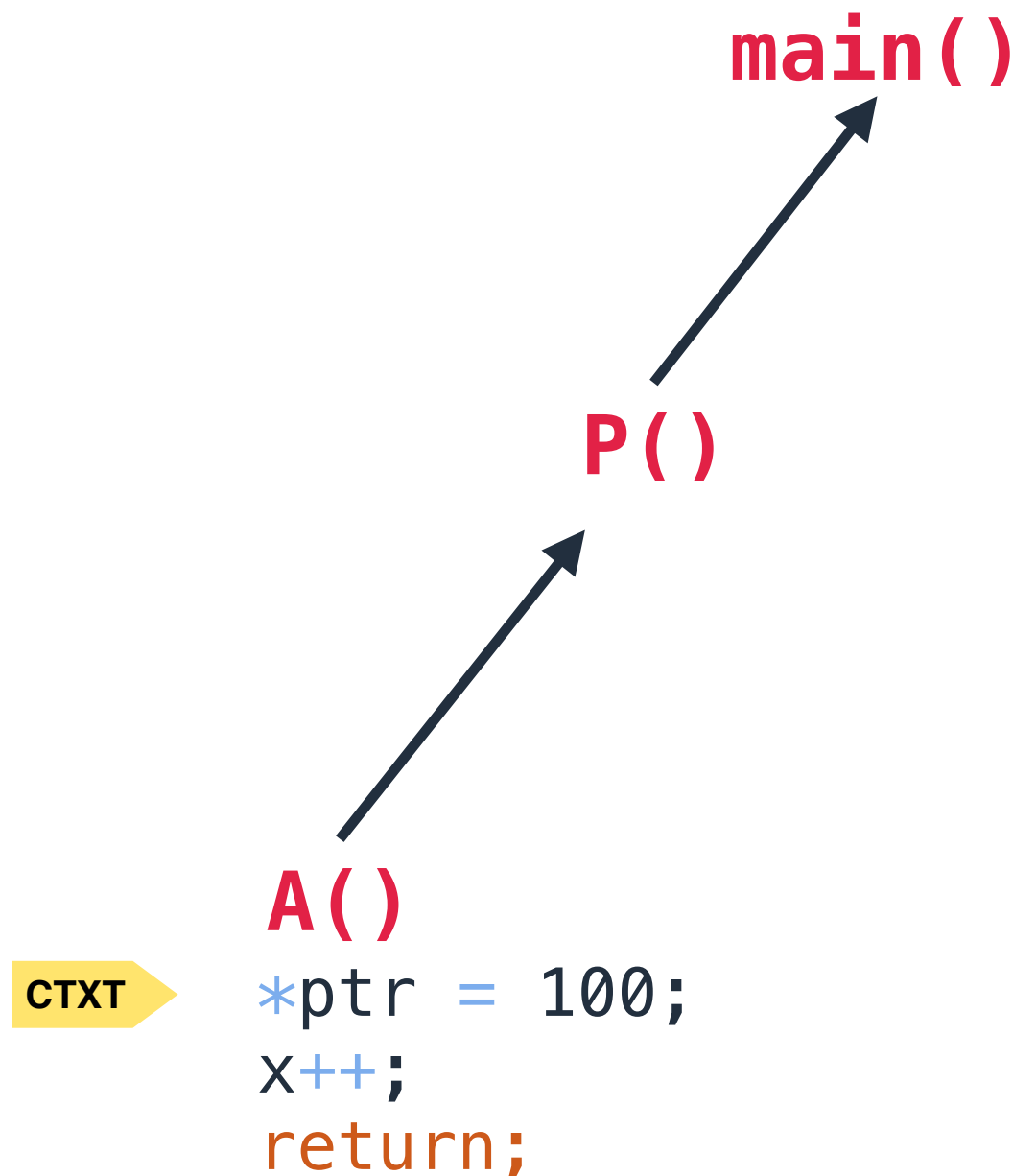
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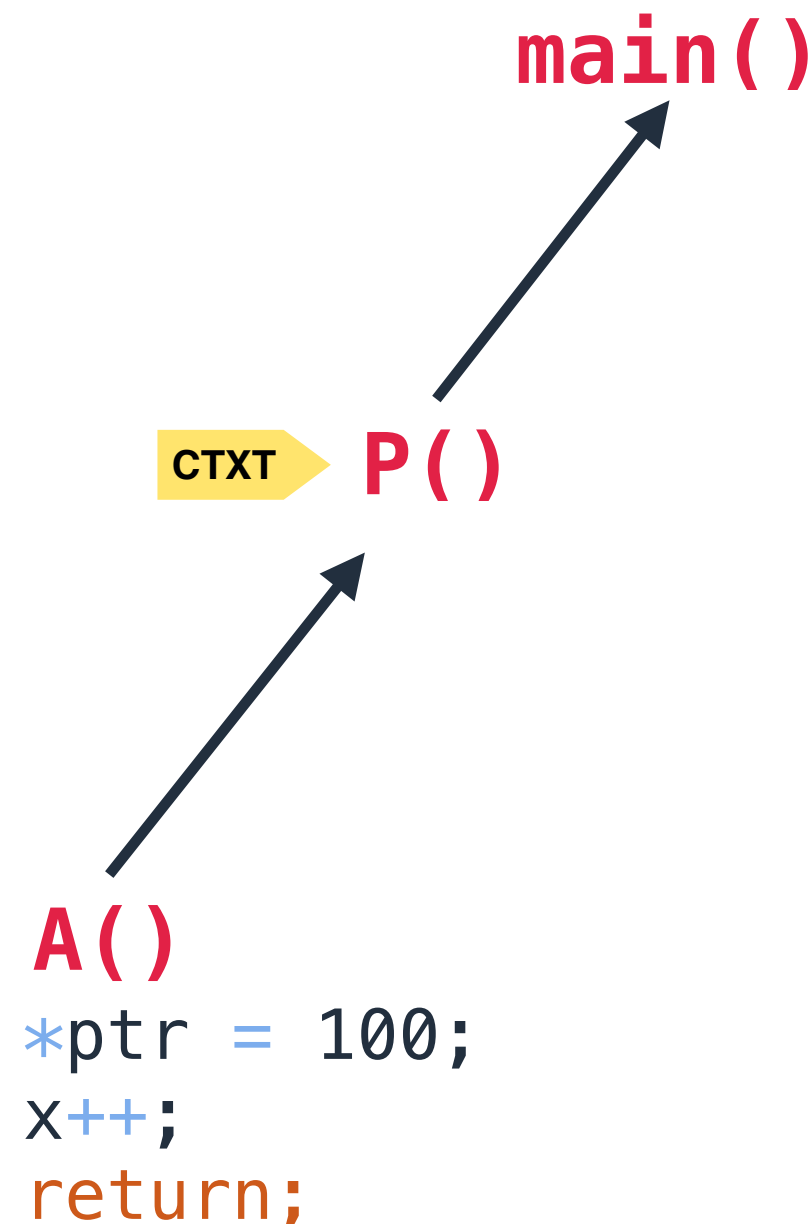


Tools can obtain
pointer to the
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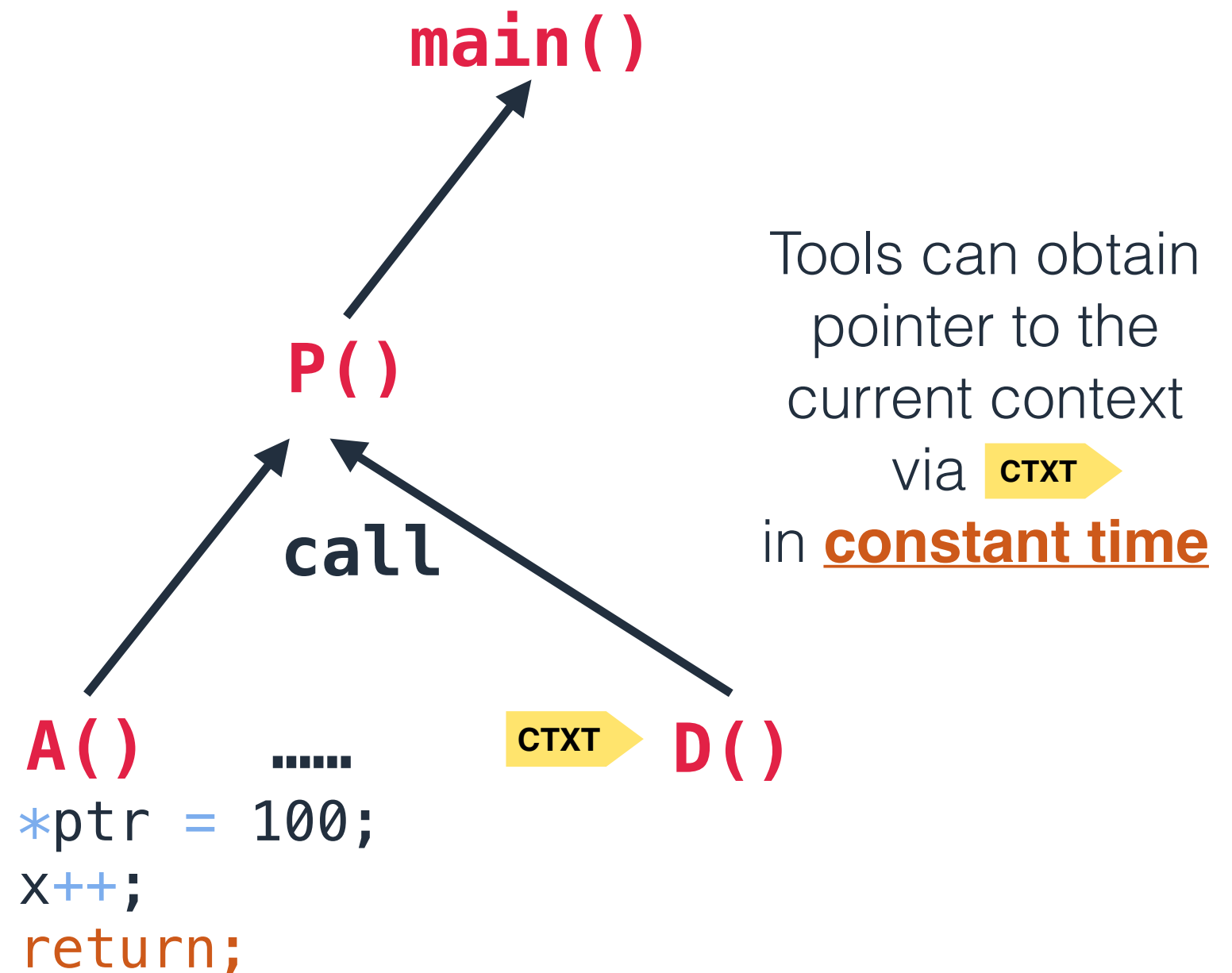
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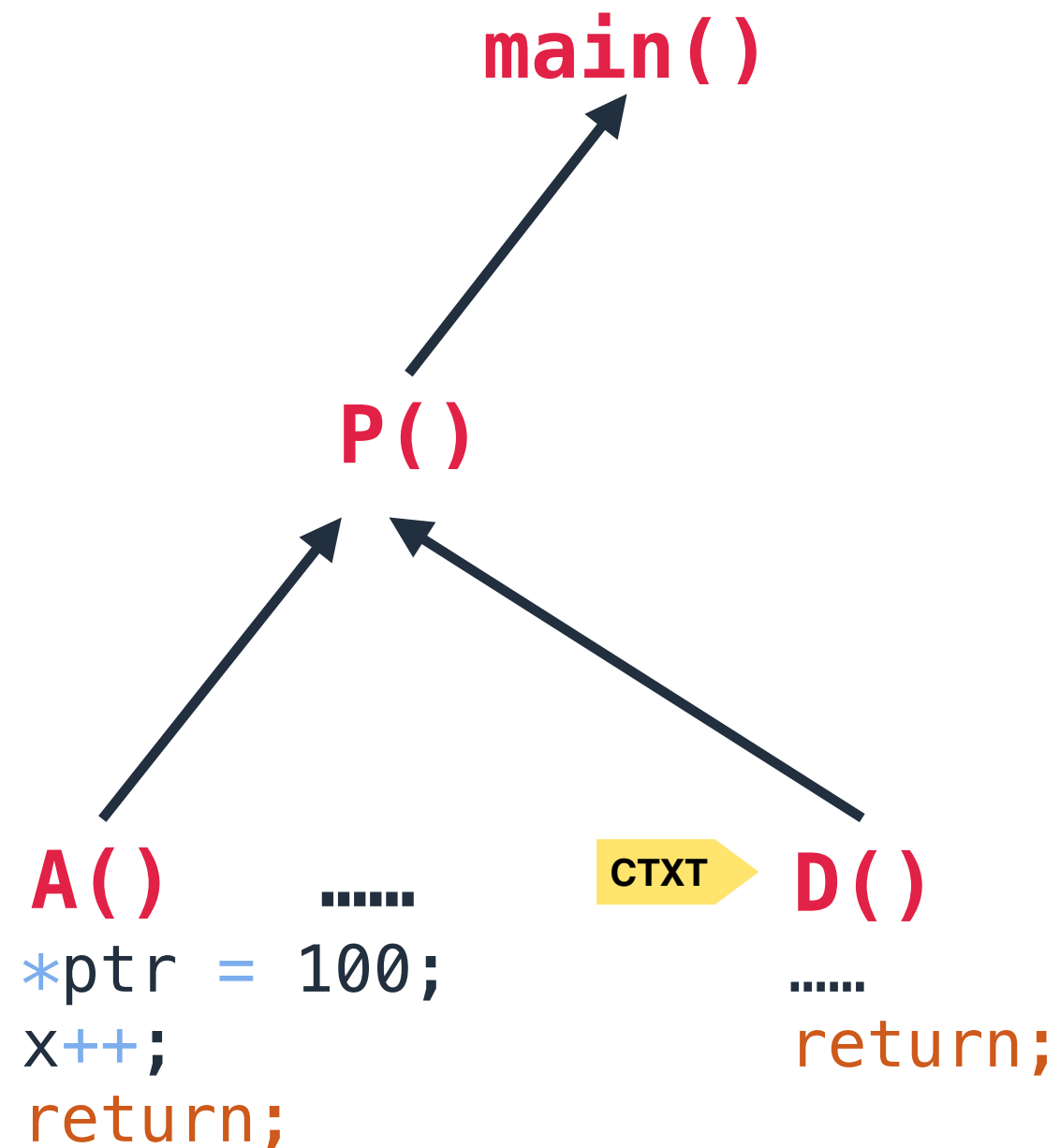
```



Shadowing Call Stack

Maintaining CTXT

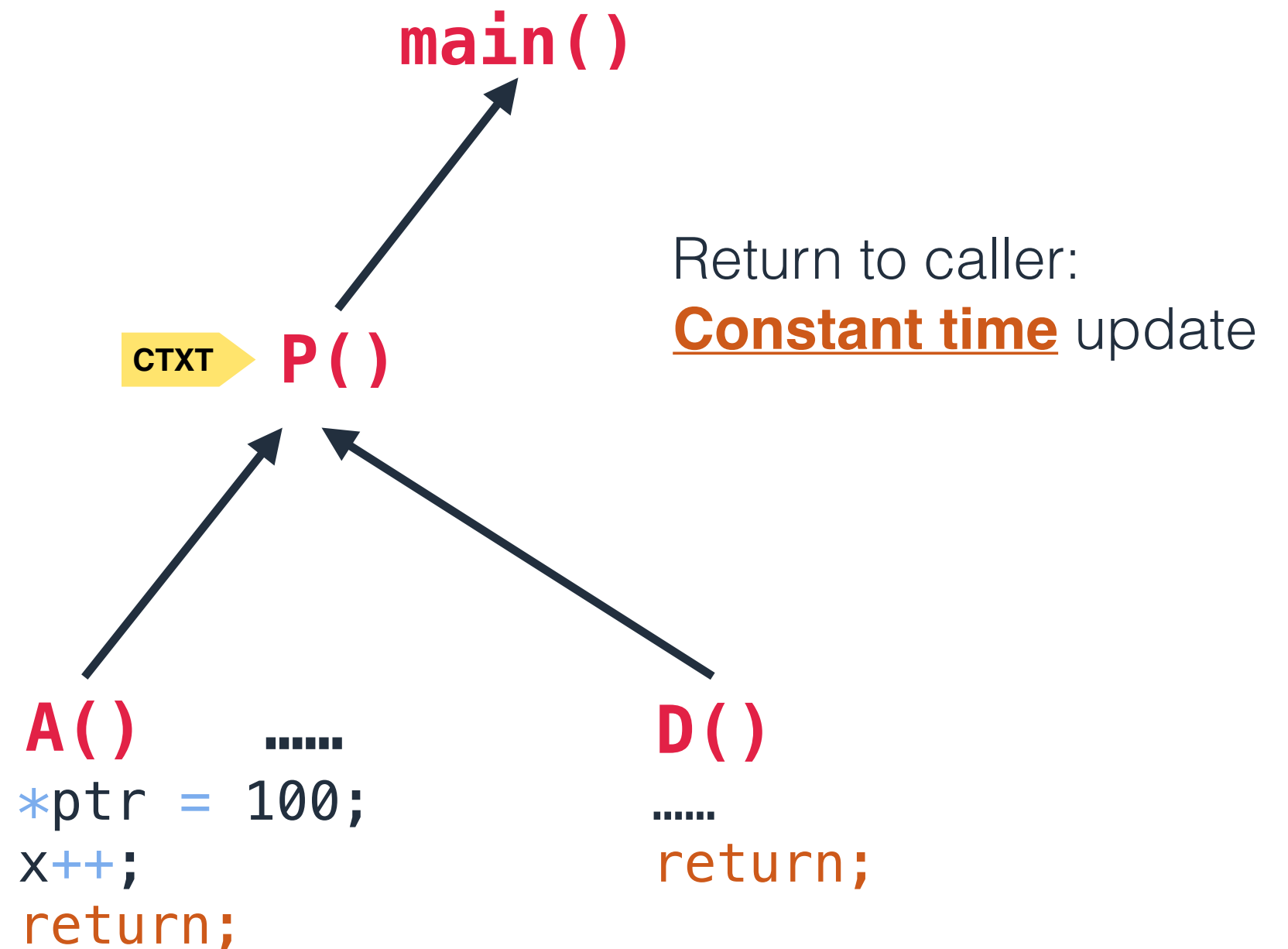
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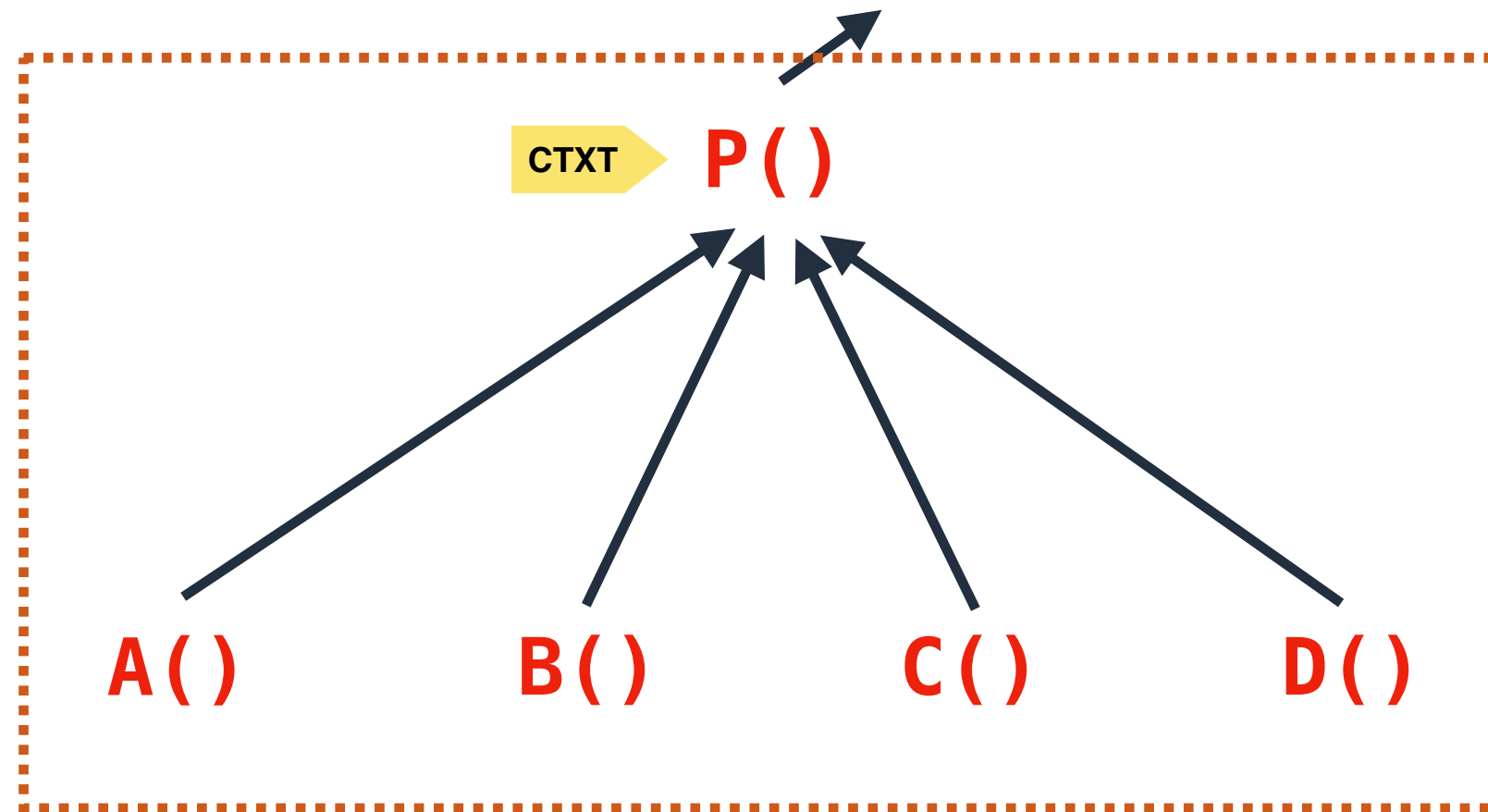
Shadowing Call Stack

Maintaining CTXT

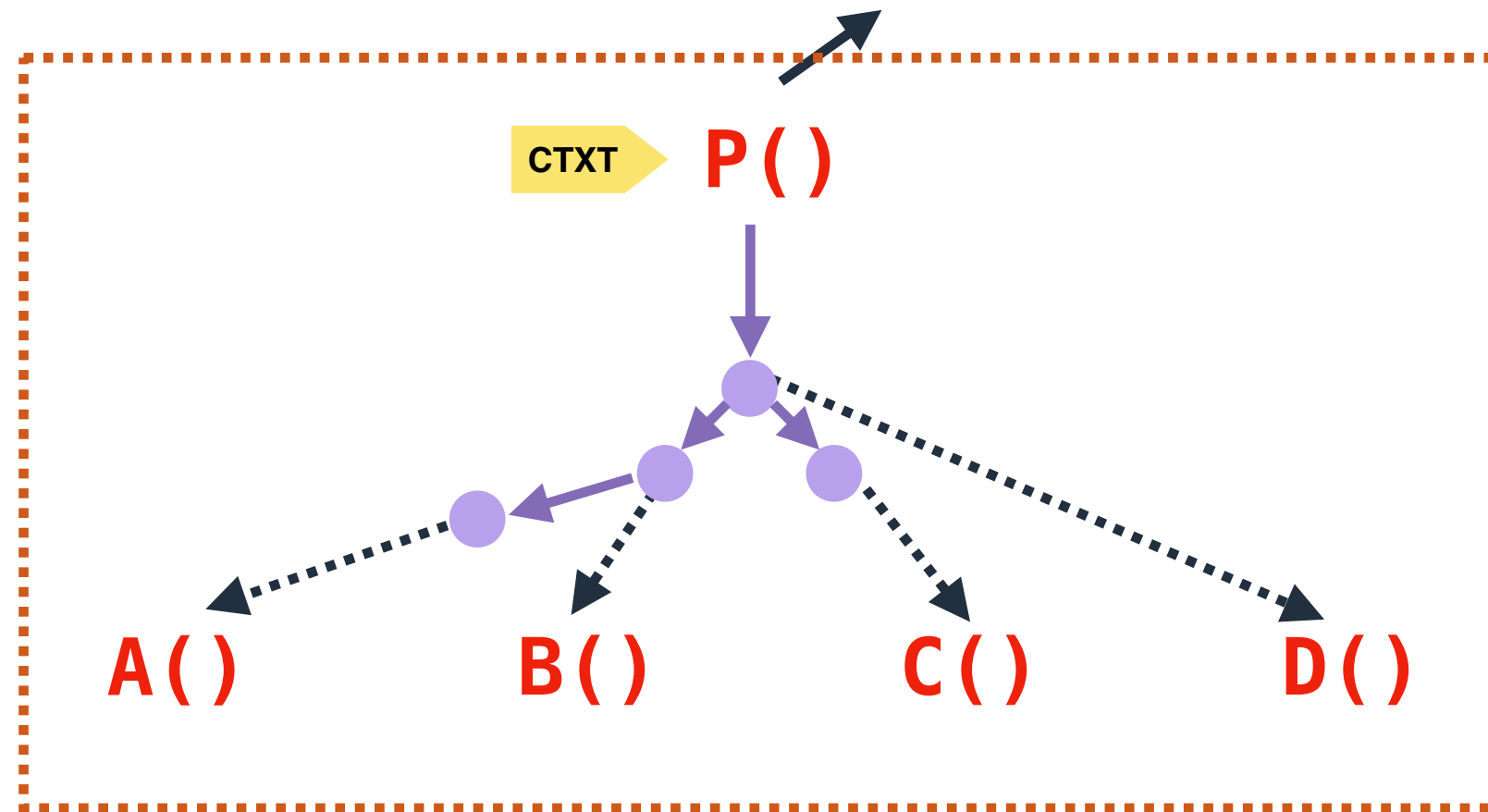
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Accelerate Lookup with Splay Trees

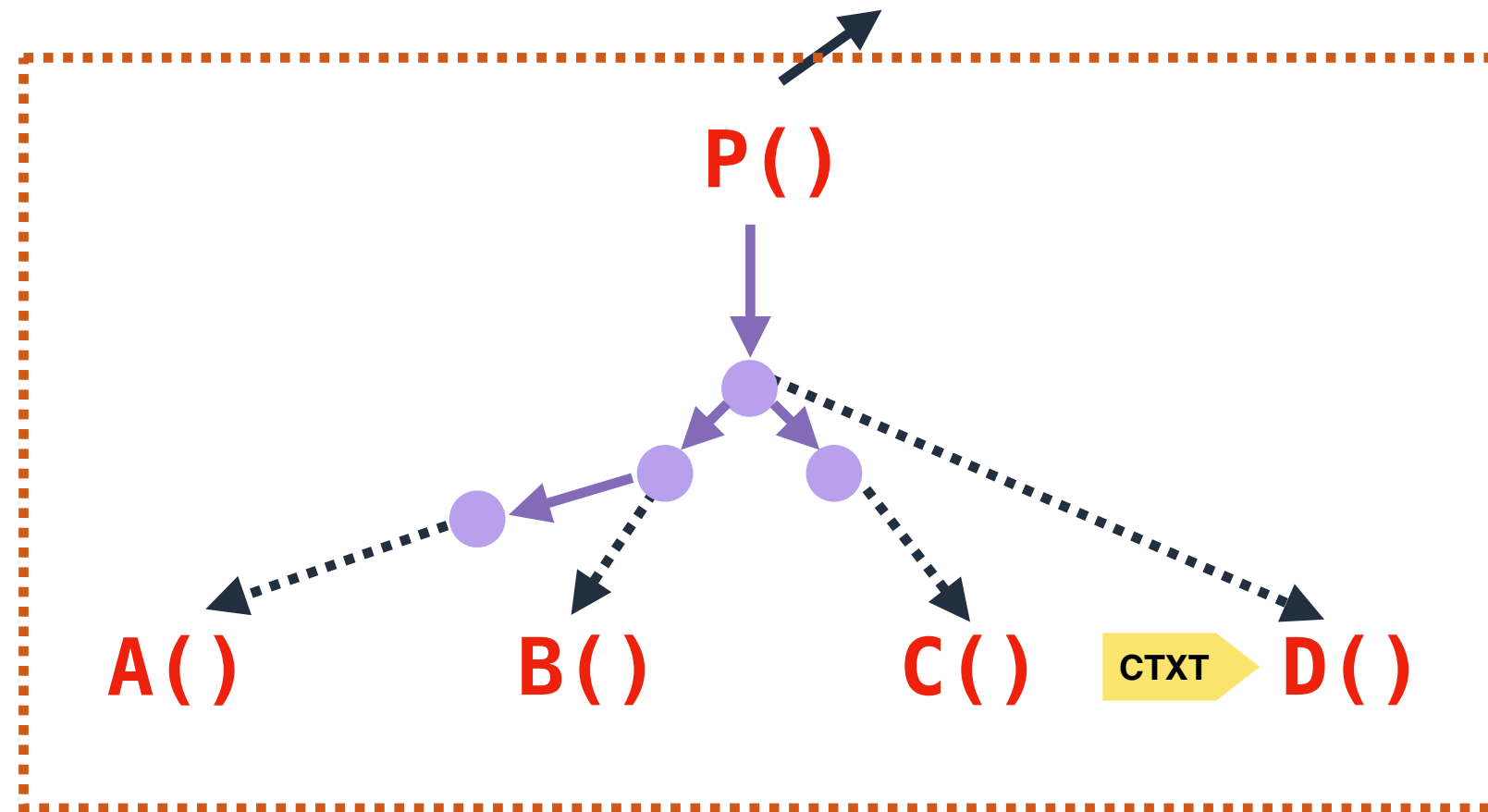


Accelerate Lookup with Splay Trees



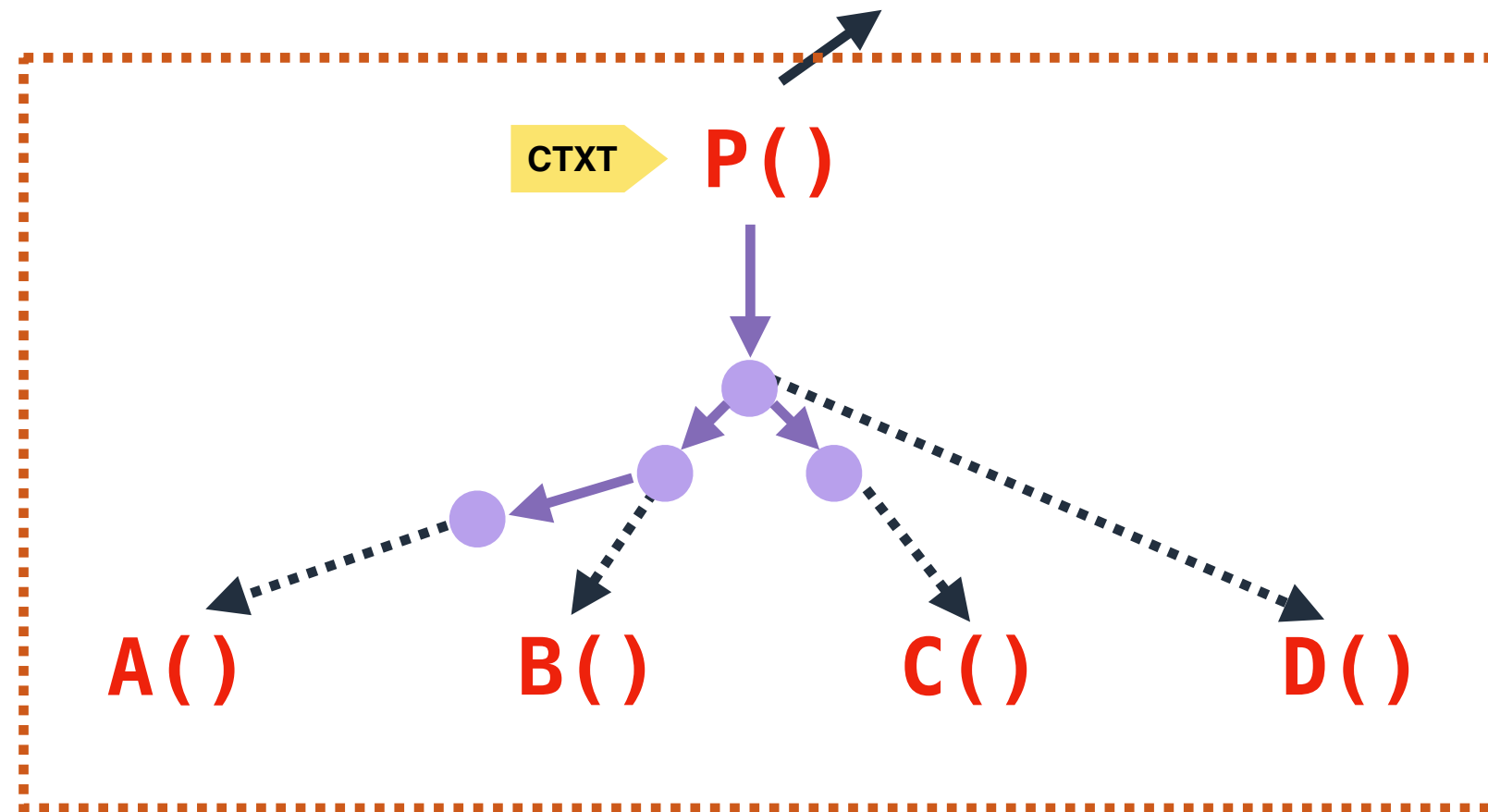
Splay tree [“Self-adjusting binary search trees” by Sleator et al. 1985]
ensures frequently called functions are near the root of the tree

Accelerate Lookup with Splay Trees



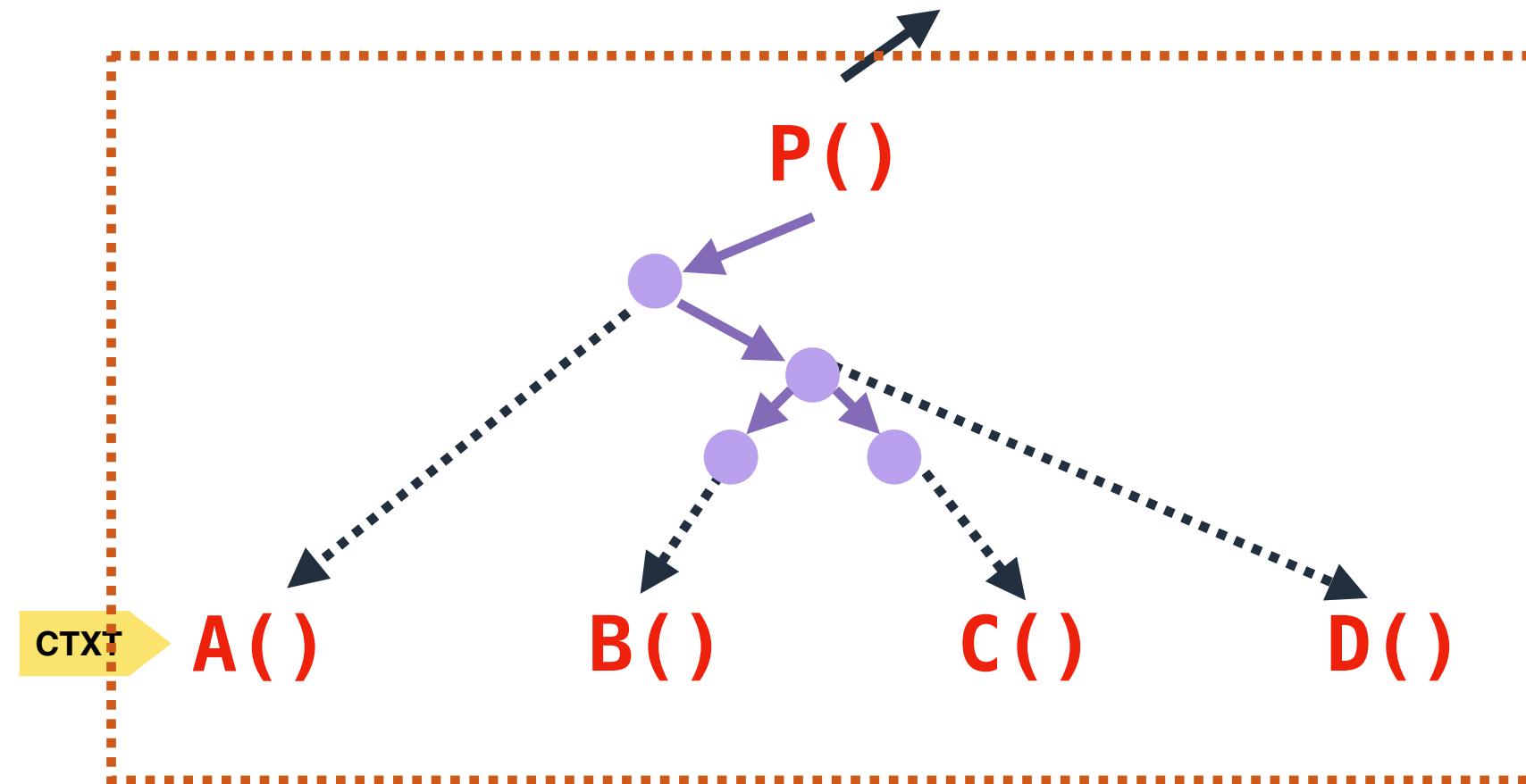
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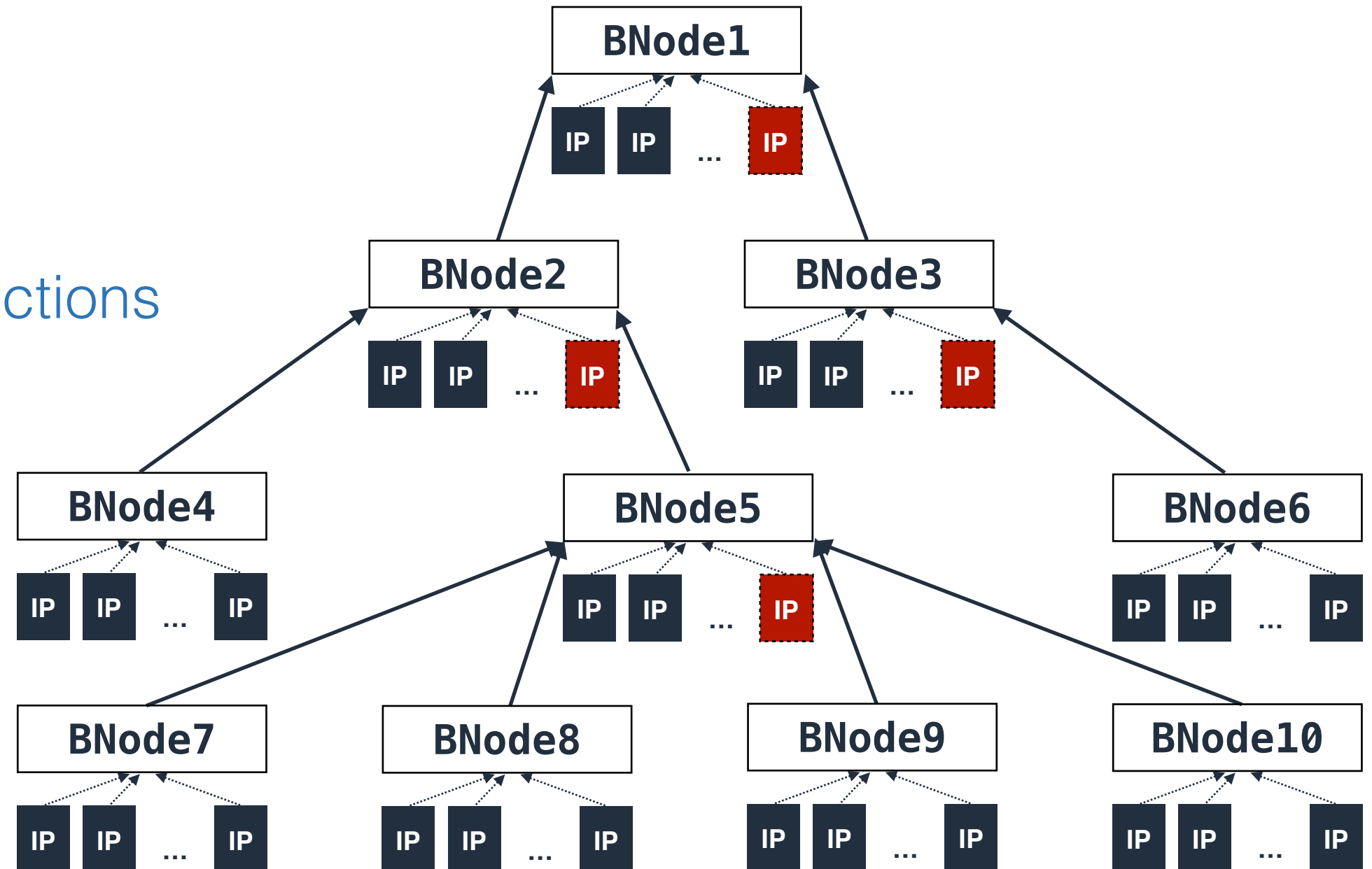
Overview of Calling Context Tree

BNode

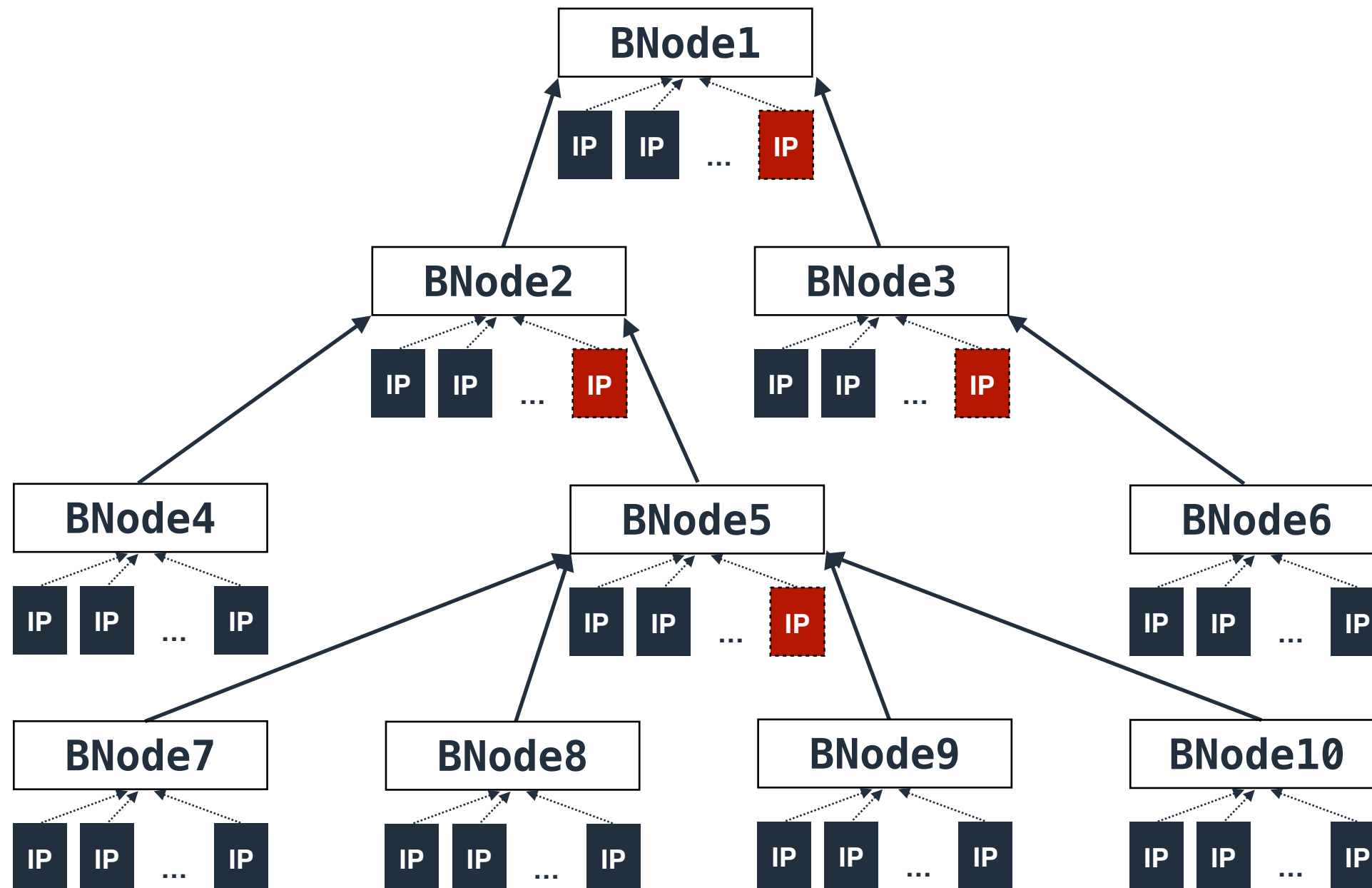
represents a basic block

IPNode

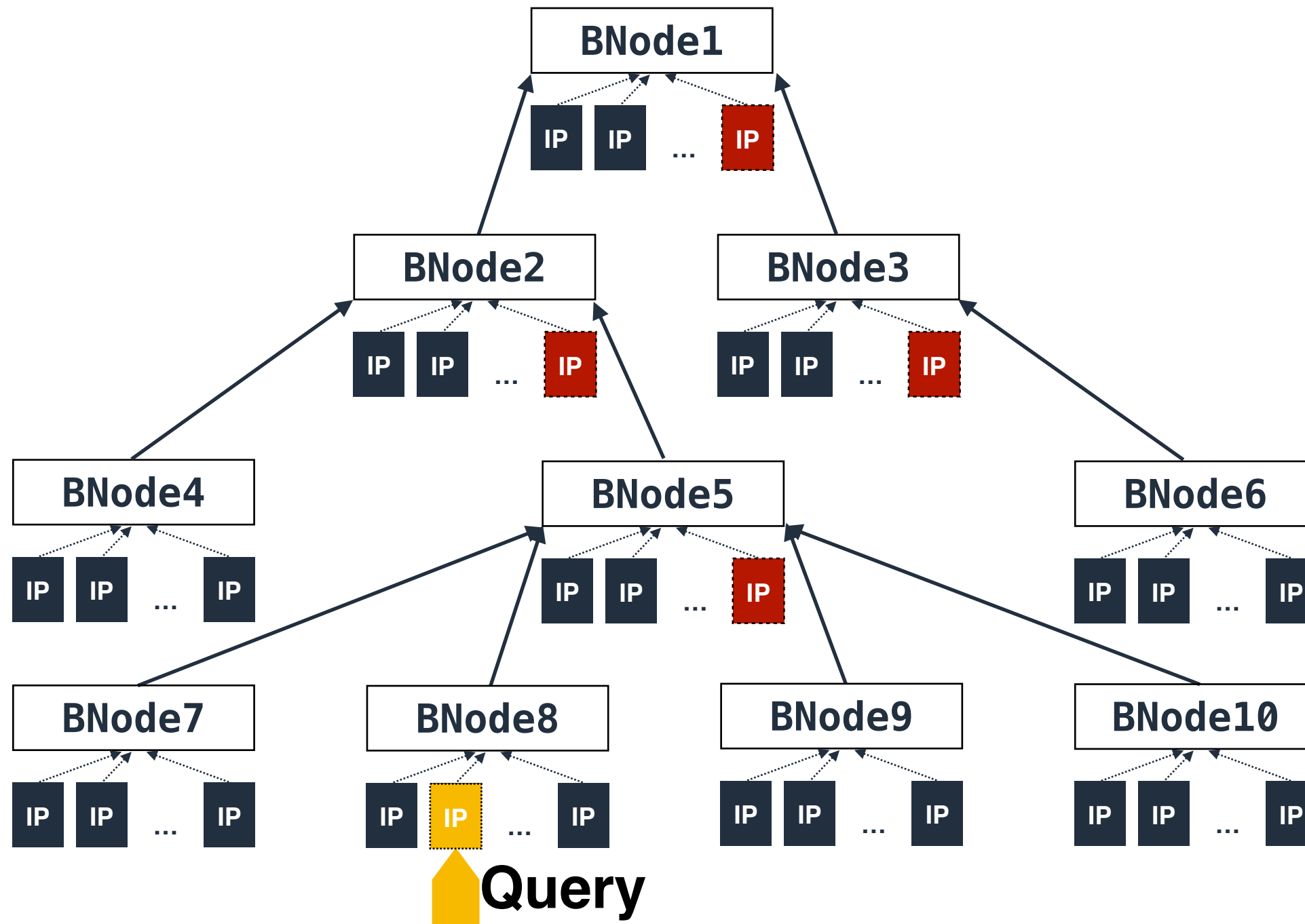
represents individual instructions



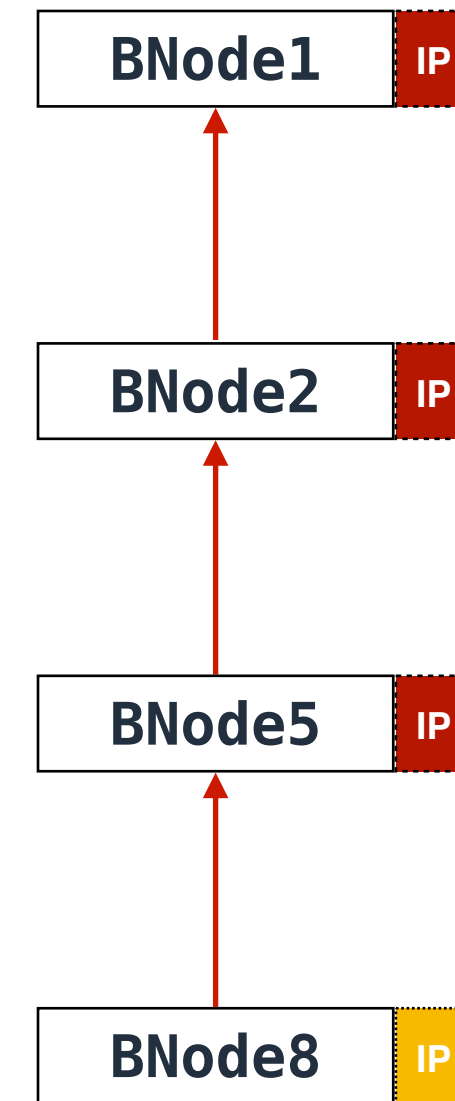
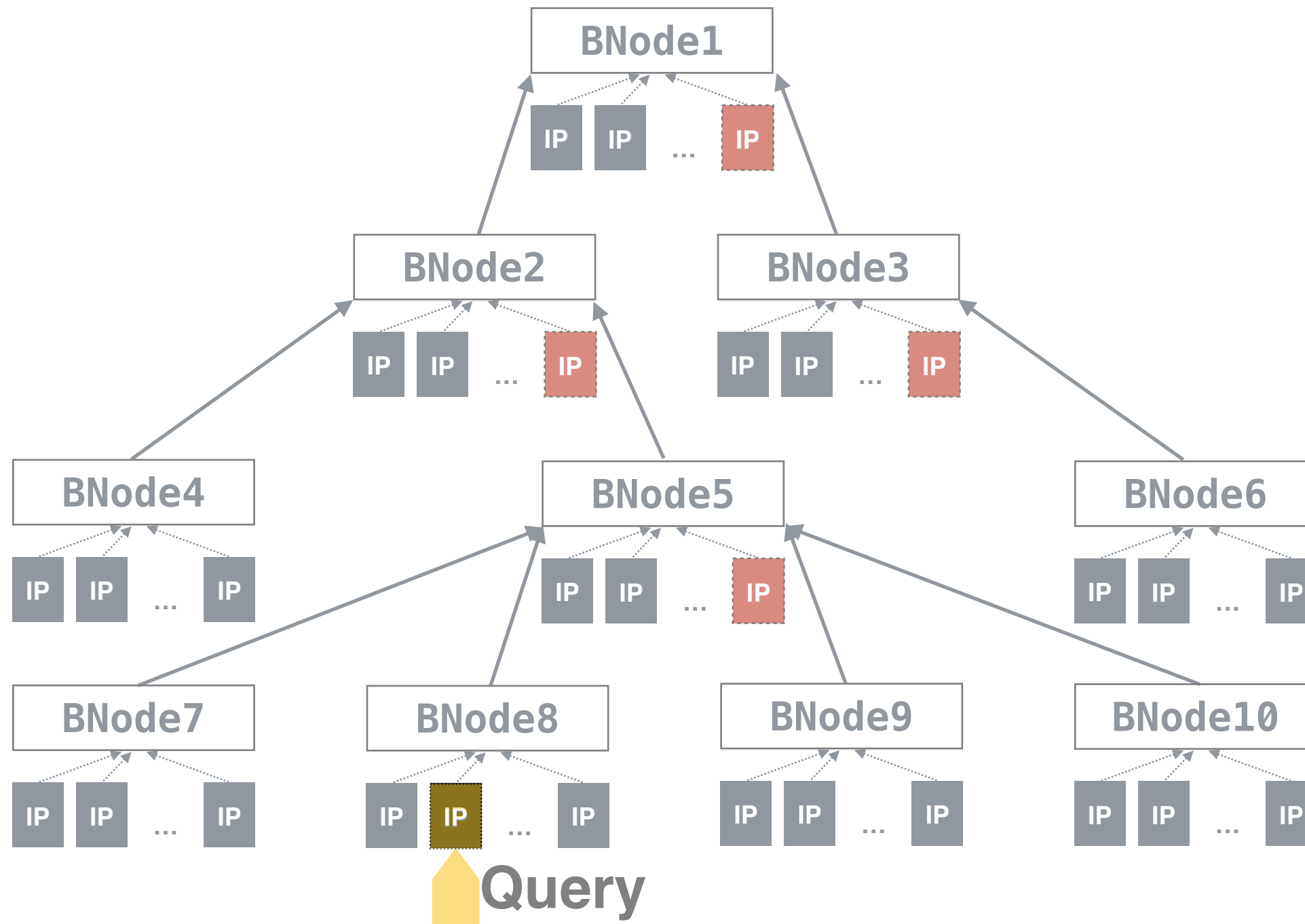
Query Call Path for Any Instruction



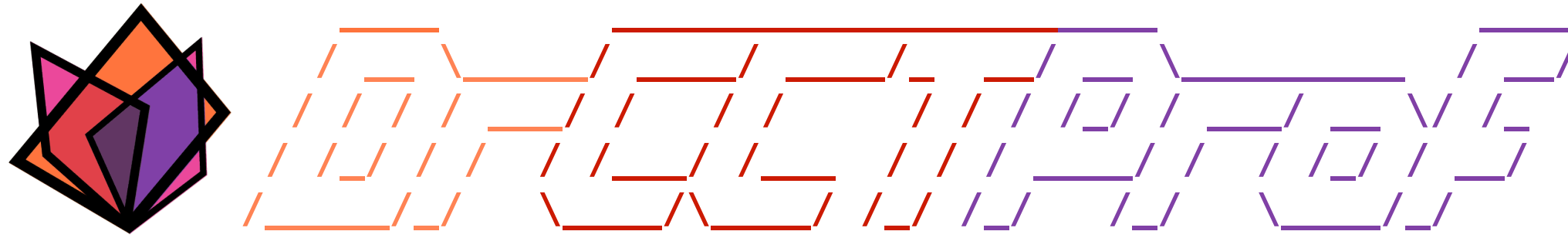
Query Call Path for Any Instruction



Query Call Path for Any Instruction



Call Path



- Ubiquitous call path collection
- **Attributing costs to data objects**
- Handling parallelism
- Evaluation
- Case study
- Conclusions

Data-Centric Attribution

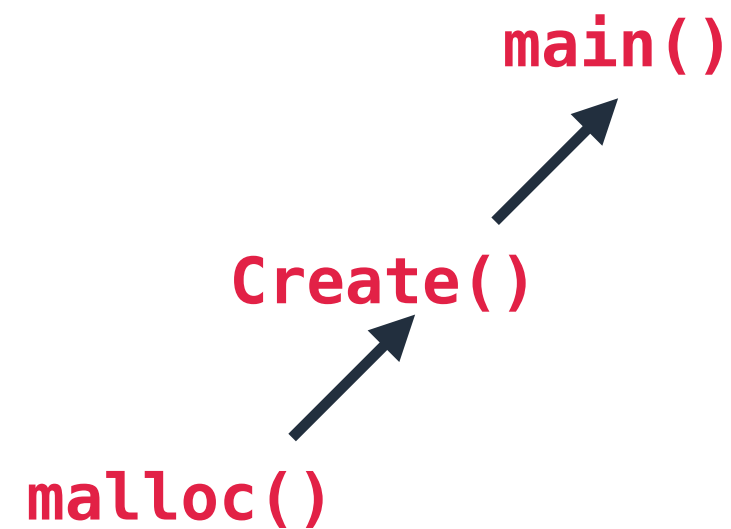
```
1:  int MyArray[s];
2:
3:  void Create(){
4:      return malloc();
5:  }
6:
7:  void Update(int * ptr){
8:      for(...)
9:          ptr[i]++;
10: }
11:
12: void main(){
13:     int *m;
14:     p1 = Create();
15:     Update(p1);
16:     p2 = MyArray;
17:     Update(p);
18: }
```

main()

Associate each data access with its **data object**

Data-Centric Attribution

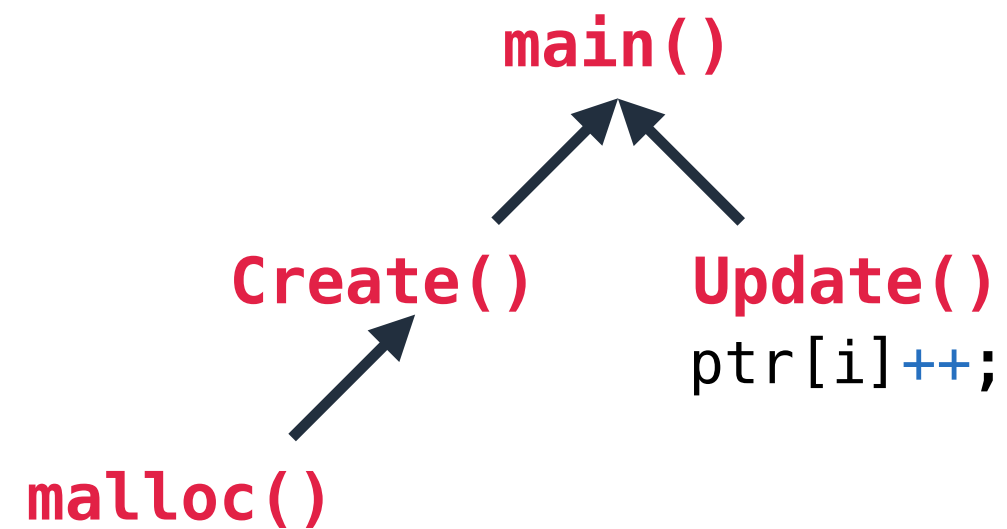
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Data-Centric Attribution

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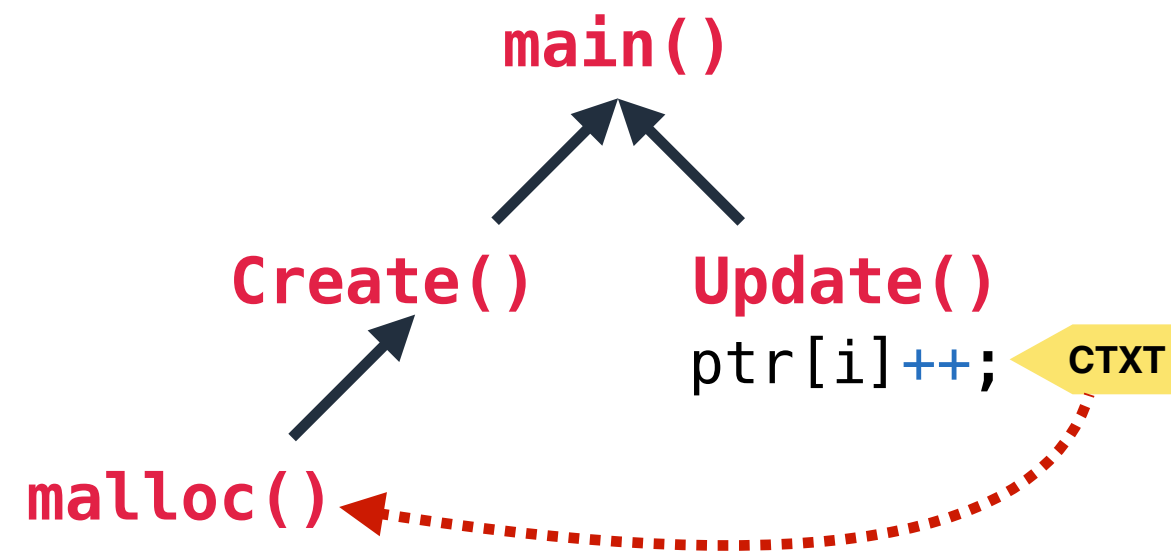
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Associate each data access with its data object

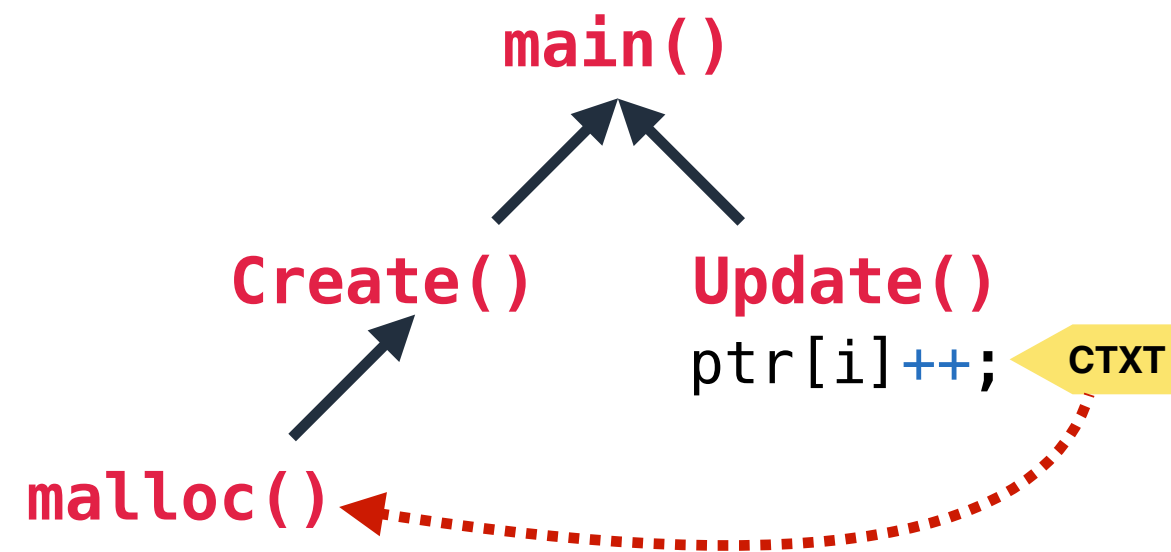
- Dynamic allocation: **Call path of allocation site**

Data-Centric Attribution

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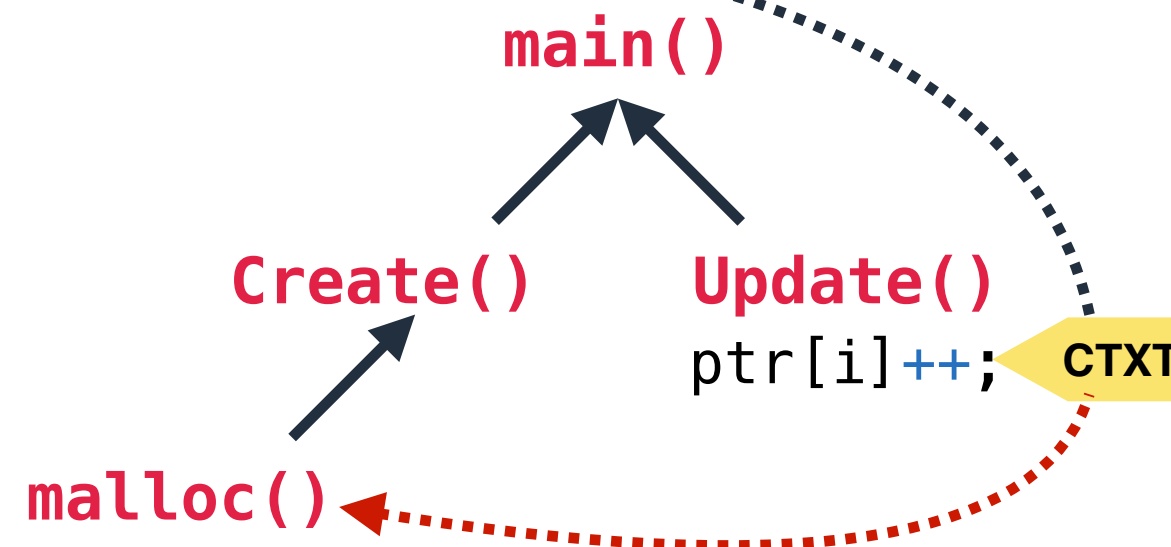
Associate each data access with its data object
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```



Associate each data access with its **data object**

- Dynamic allocation: **Call path of allocation site**
- Static objects: **Variable name**

Data-Centric Attribution

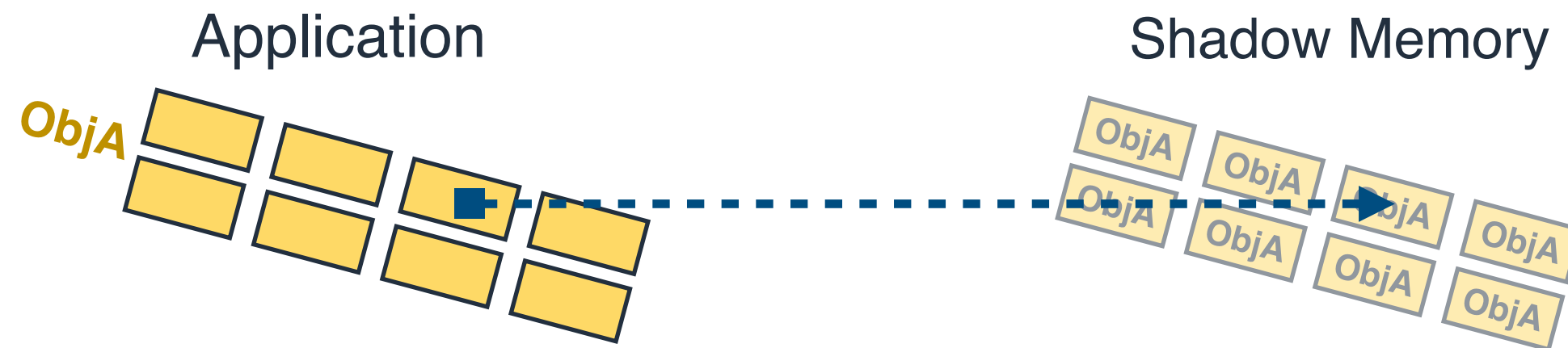
Implementation

Shadow memory

Data-Centric Attribution

Implementation

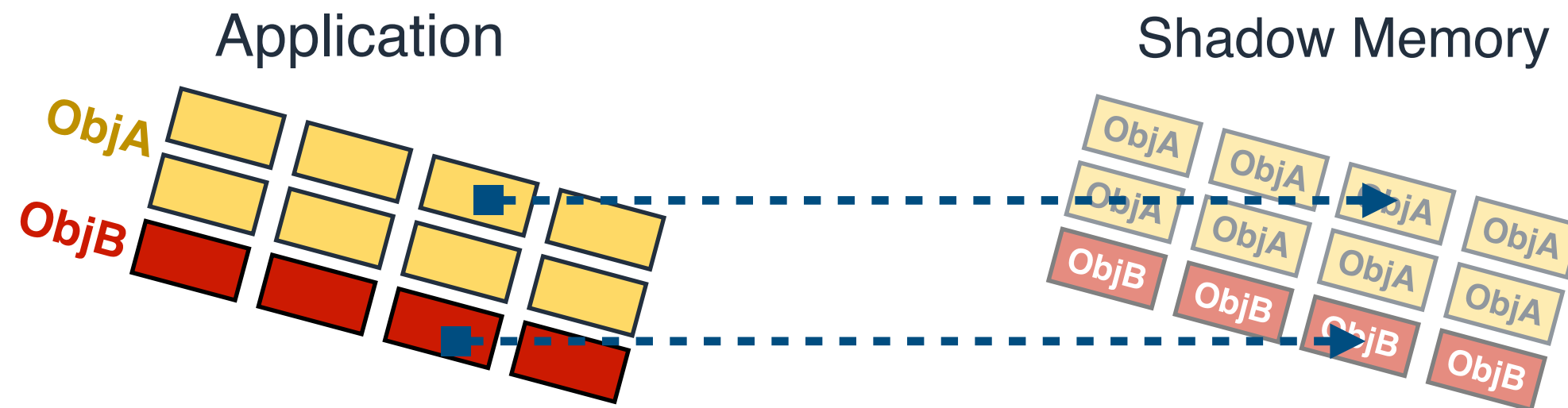
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Data-Centric Attribution

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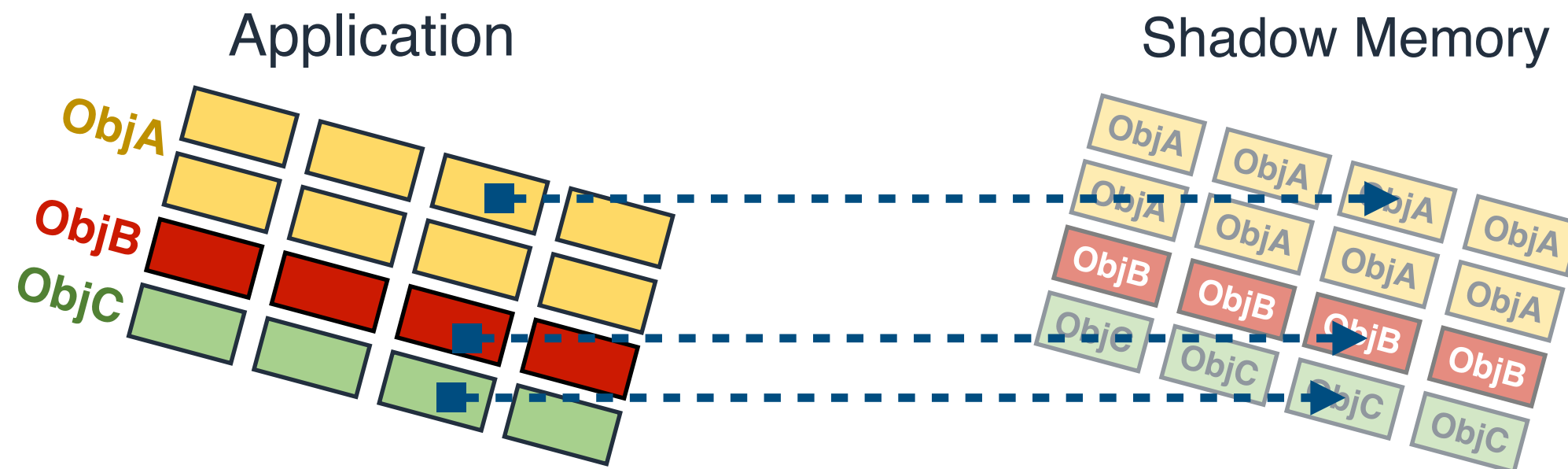
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Data-Centric Attribution

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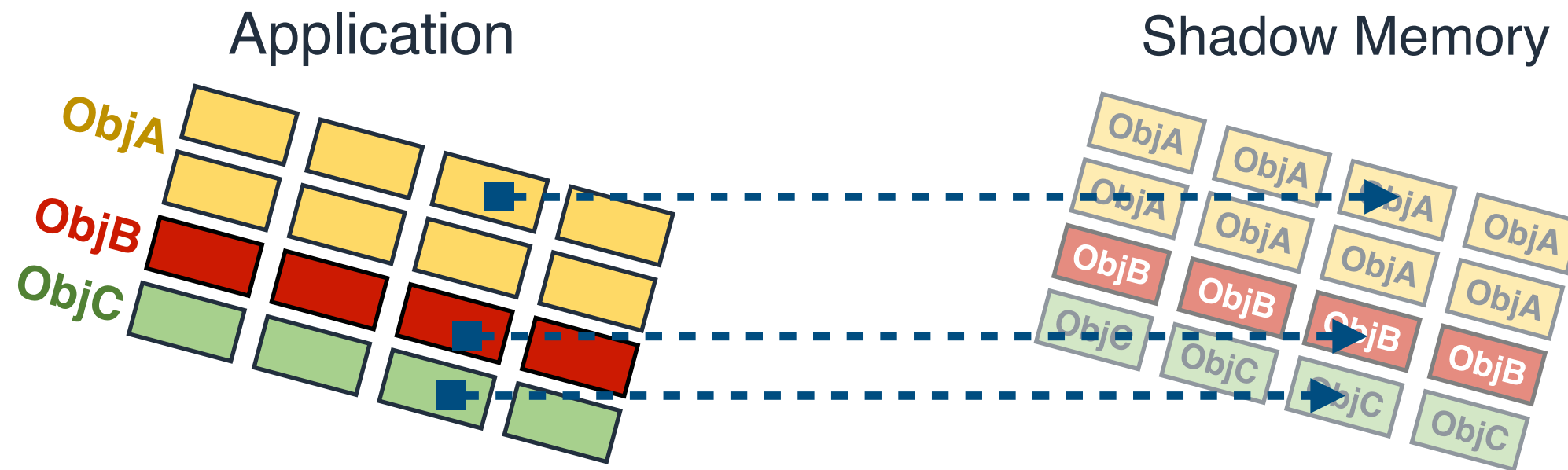
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Data-Centric Attribution

Implementation

Shadow memory



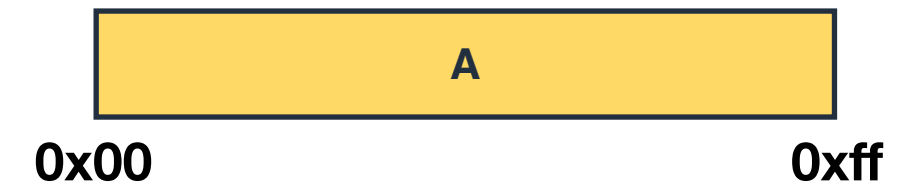
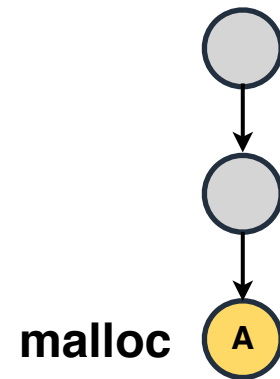
For each memory cell, a shadow cell holds a handle for the memory cell's data object

- Low lookup cost— **$O(1)$**
- Support **concurrent** access

Correlate Data-Attribution with Code-Attribution

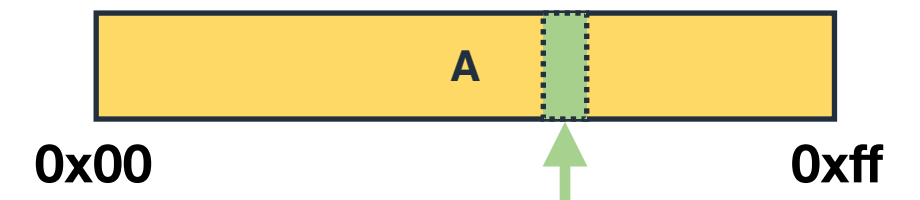
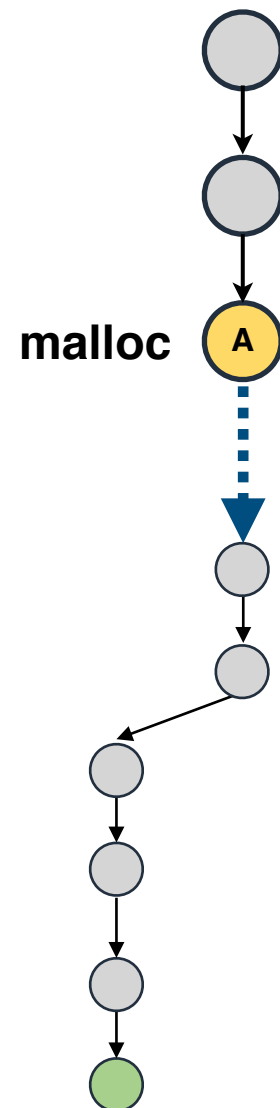
Correlate Data-Attribution with Code-Attribution

CCT for
heap allocated variables



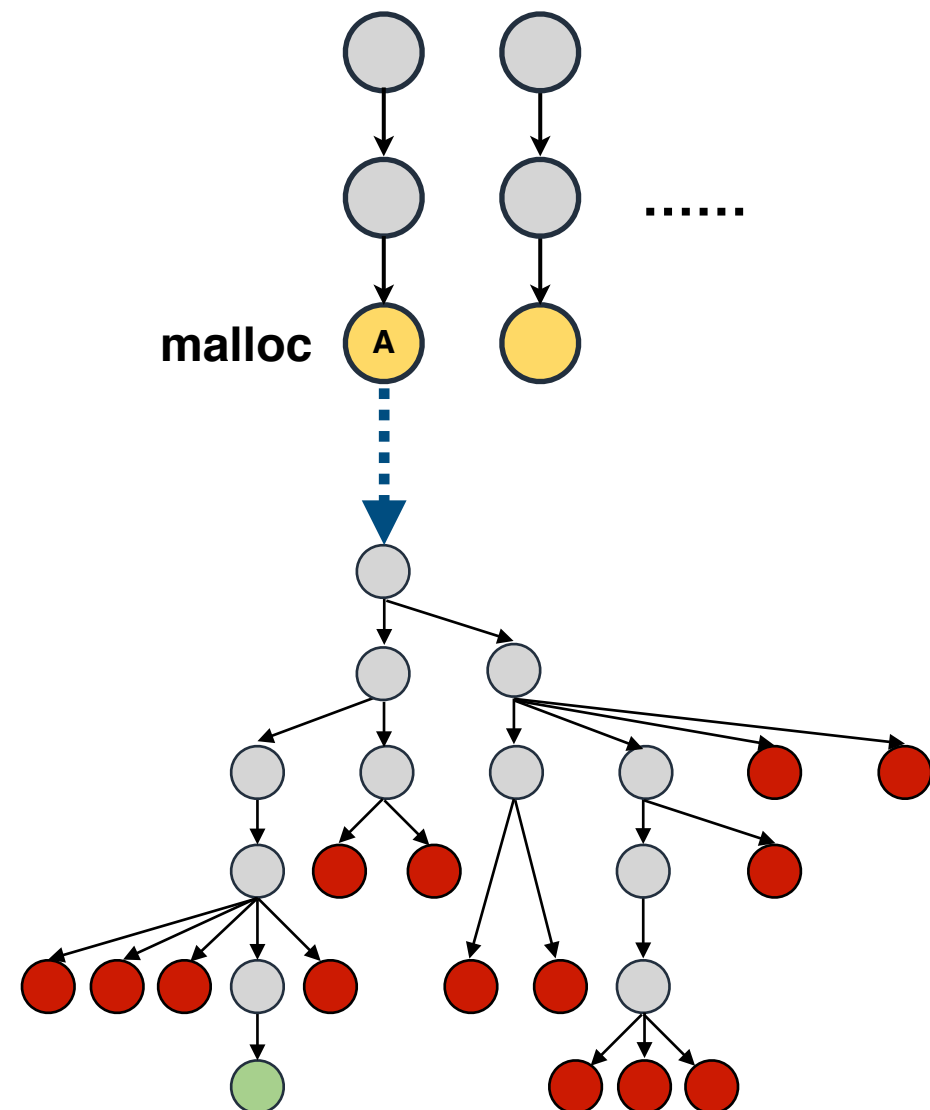
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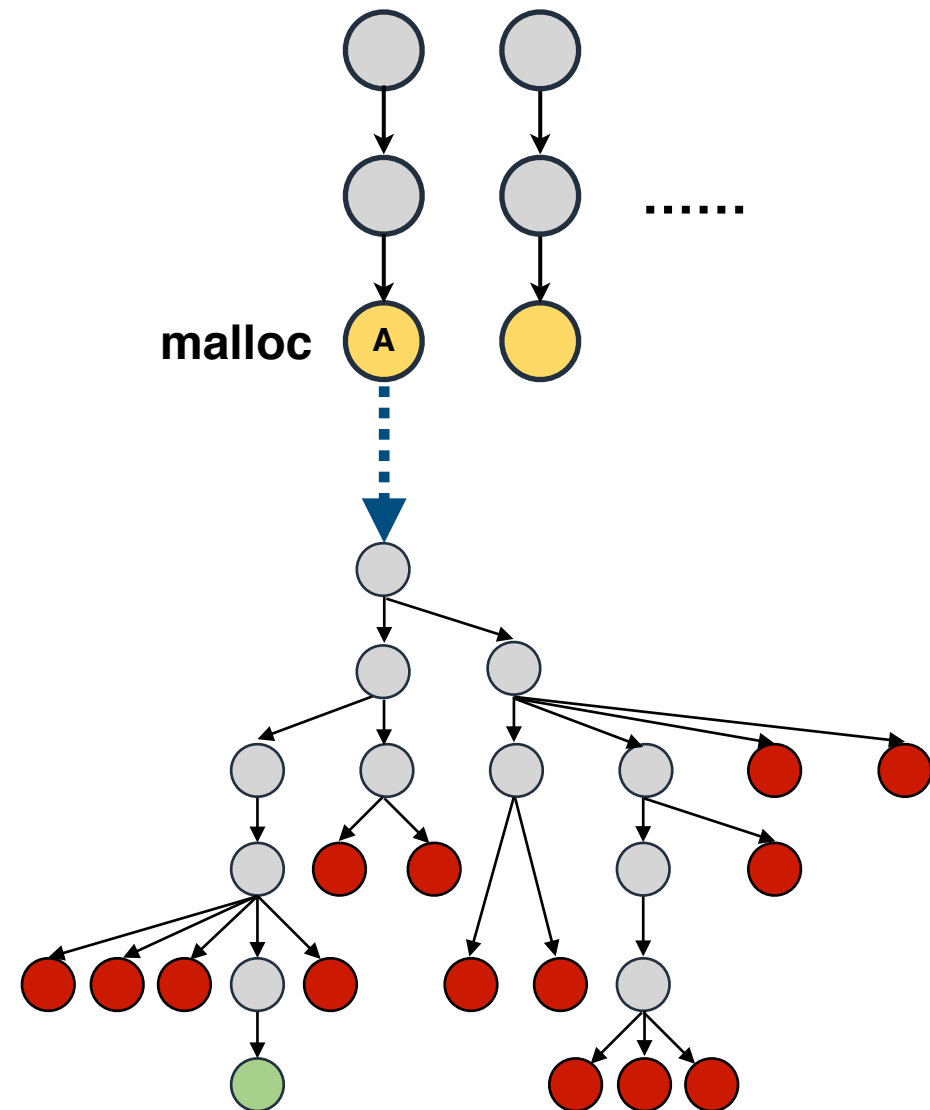
Correlate Data-Attribution with Code-Attribution

**CCT for
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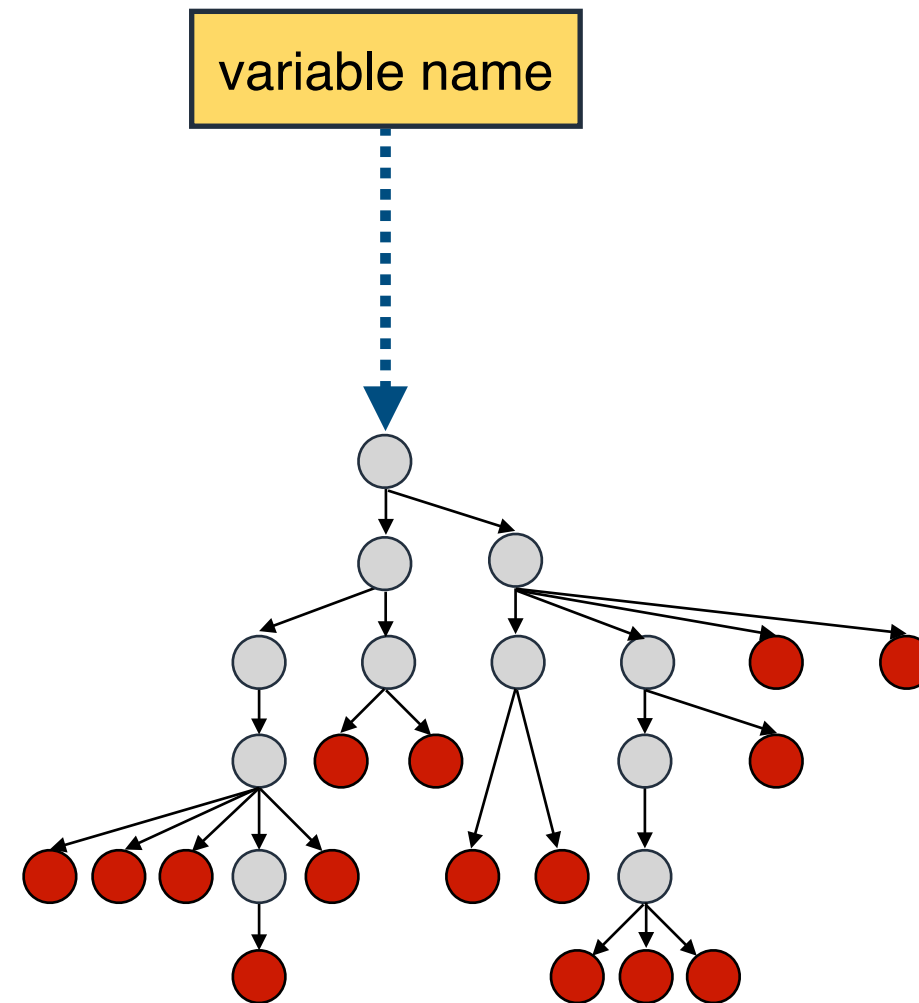


Correlate Data-Attribution with Code-Attribution

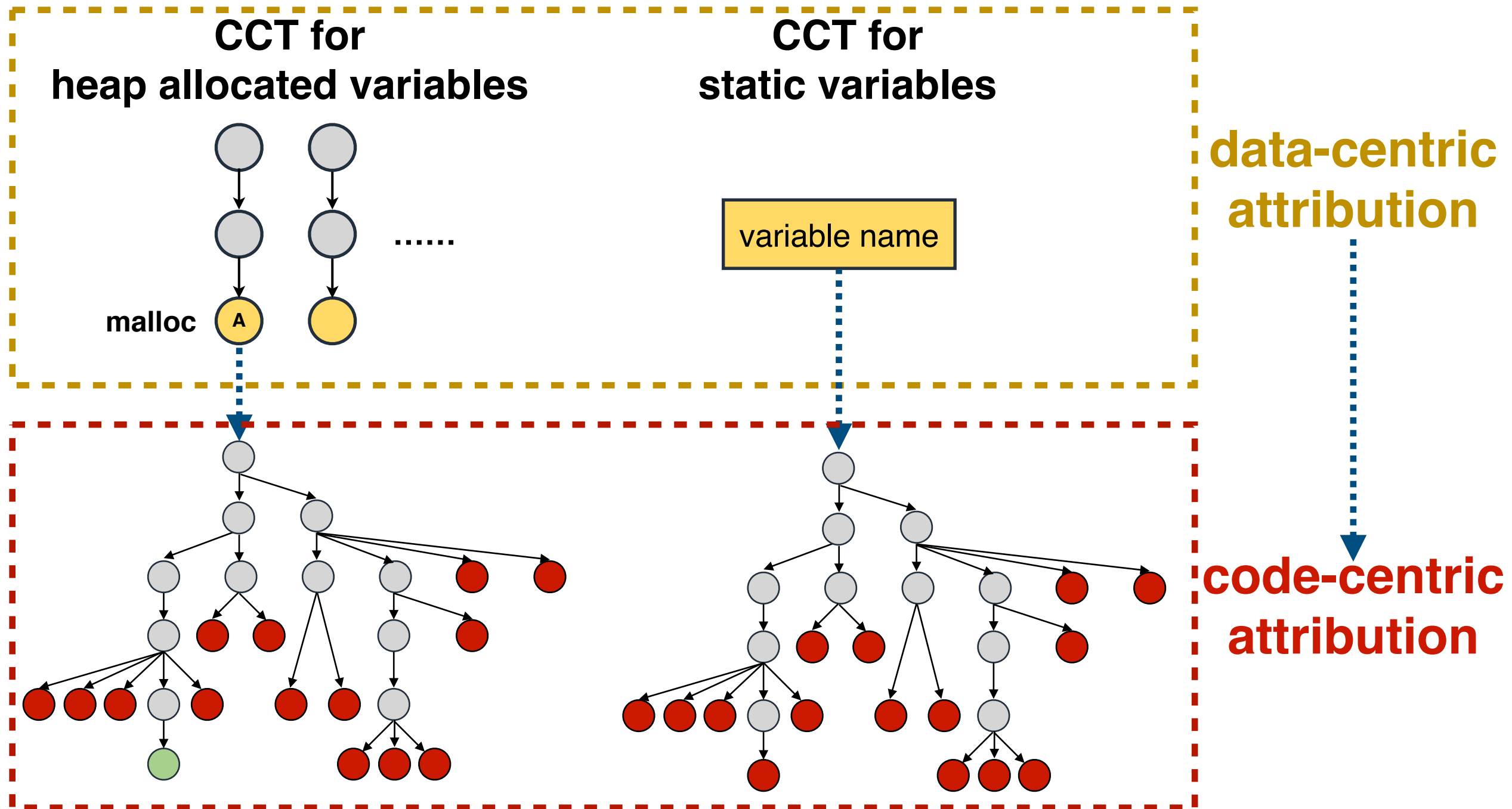
CCT for
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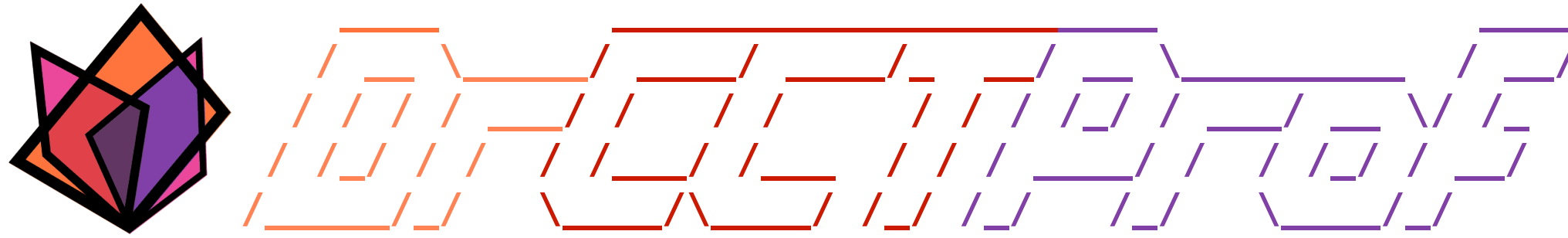


CCT for
static variables



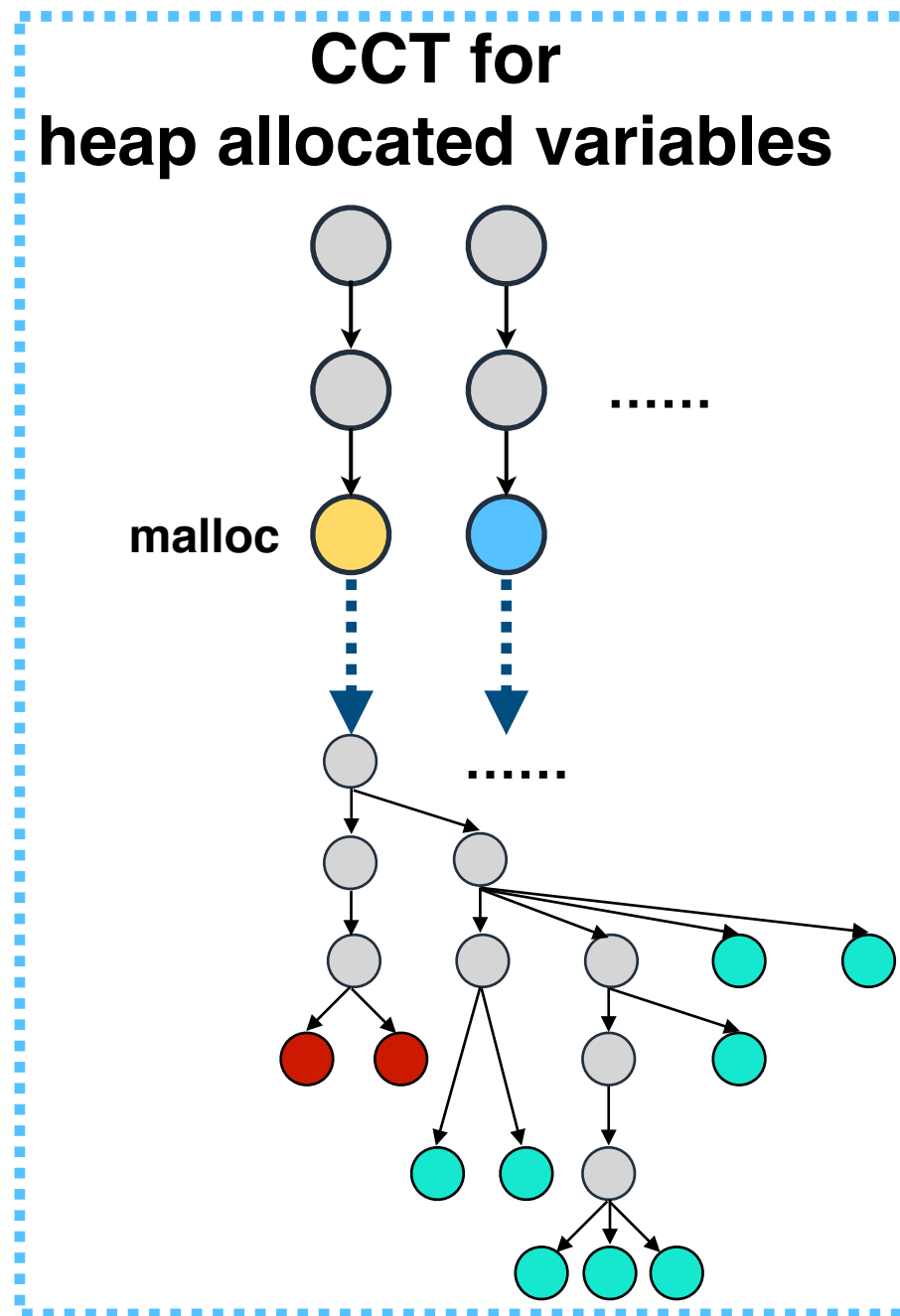
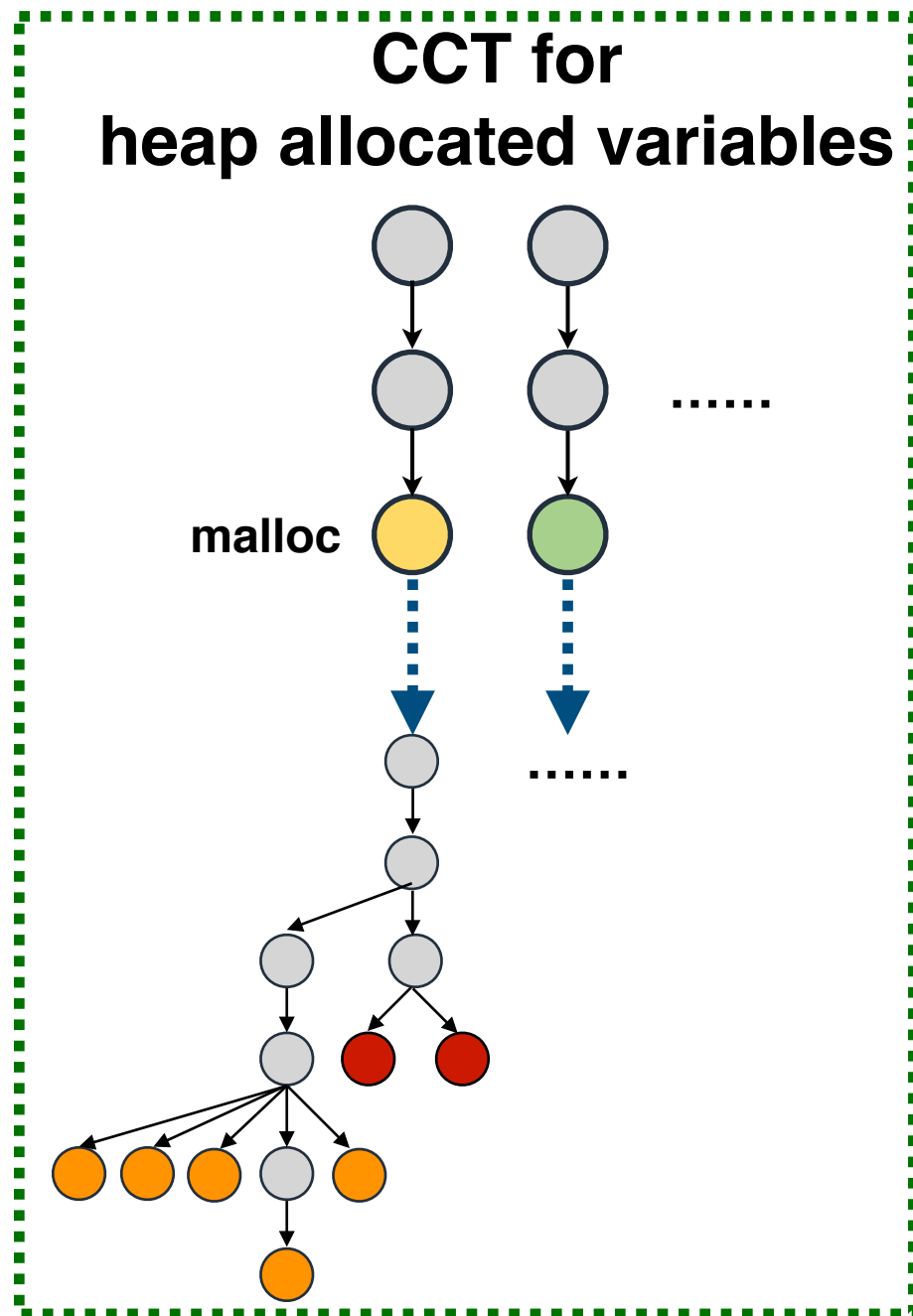
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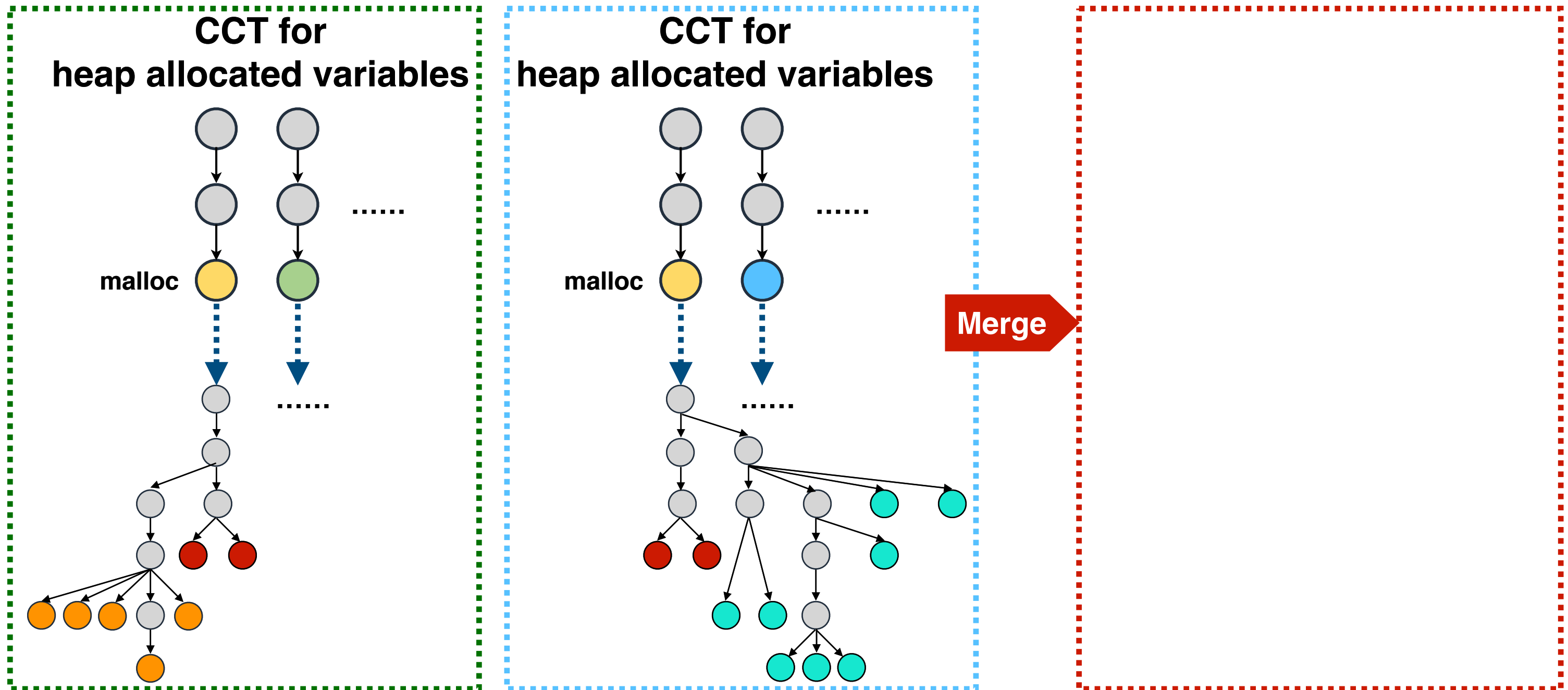


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- Attributing costs to data objects
- **Handling parallelism**
- Evaluation
- Case study
- Conclusions

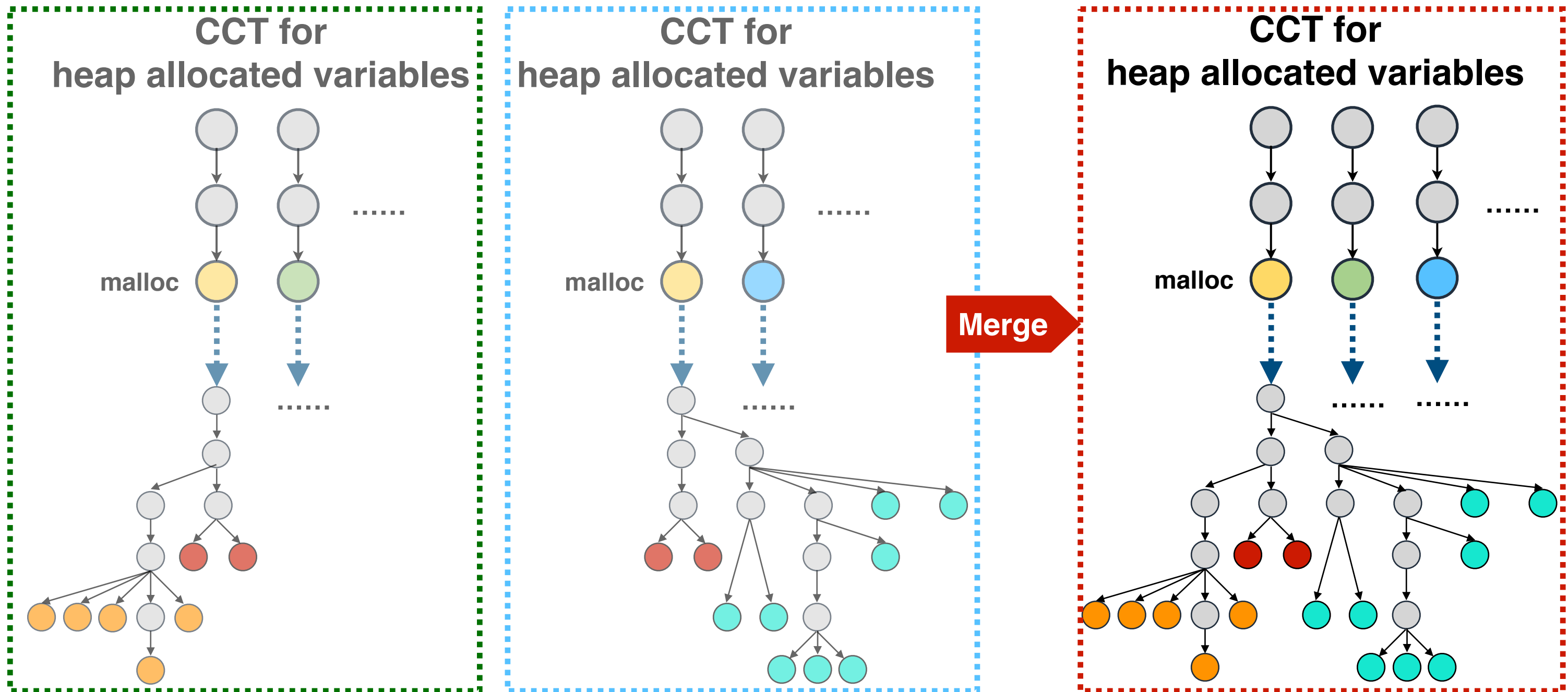
Merge CCTs across Threads/Processes

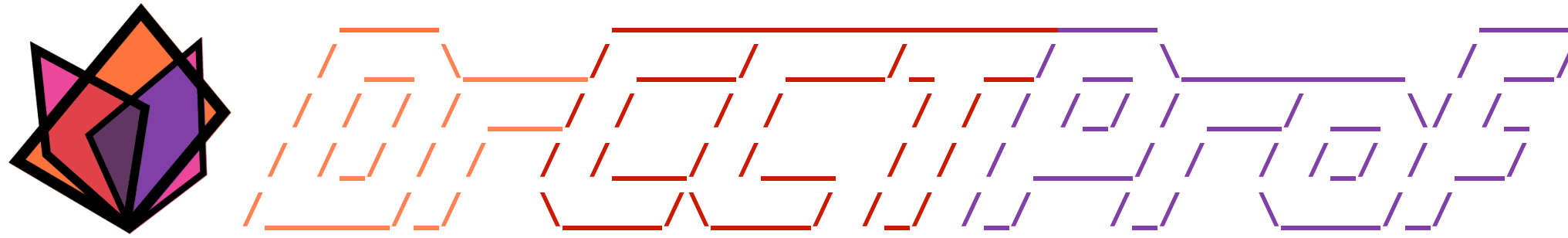


Merge CCTs across Threads/Processes



Merge CCTs across Threads/Processes





- Ubiquitous call path collection
- Attributing costs to data objects
- Merge attributions
- **Evaluation**
- Case study
- Conclusions

DrCCTProf Overhead

Machine configuration

Cluster

- Amazon Web Services (AWS)

Node

- Graviton2 CPU **32 ARMv8 cores** clocked at 2.3 GHz

Workloads

NPB-MPI

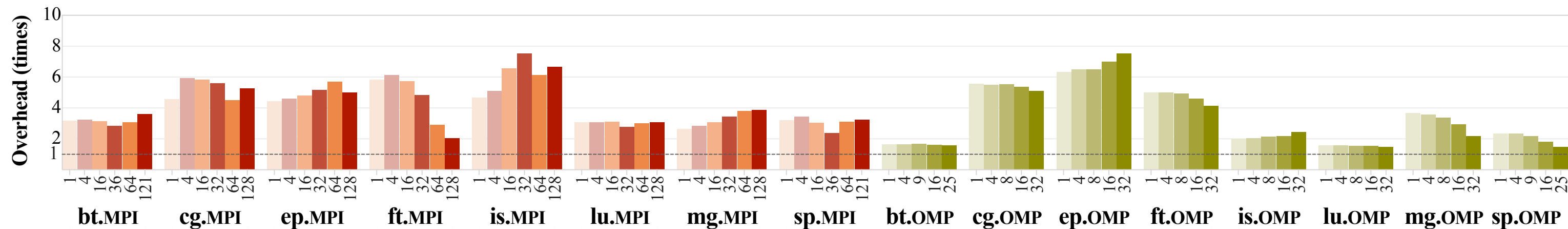
- **4 Nodes 128 processes in total**

NPB-OMP

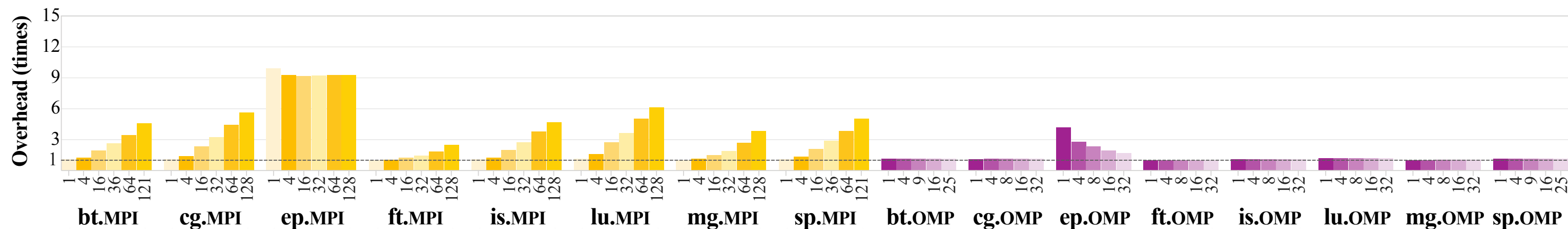
- **1 Node 32 threads in total**

NPB Code-Centric Overhead

Runtime Overhead [**MPI 3.8x**] [**OMP 2.3x**]

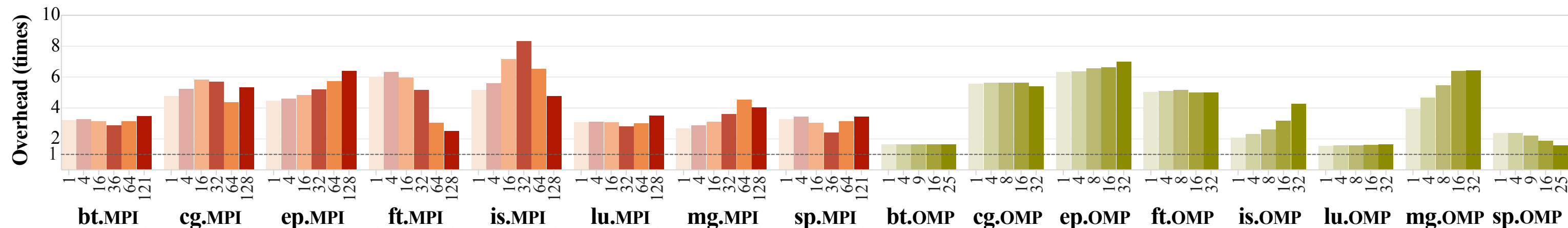


Space Overhead [**MPI 4.9x**] [**OMP 1.2x**]

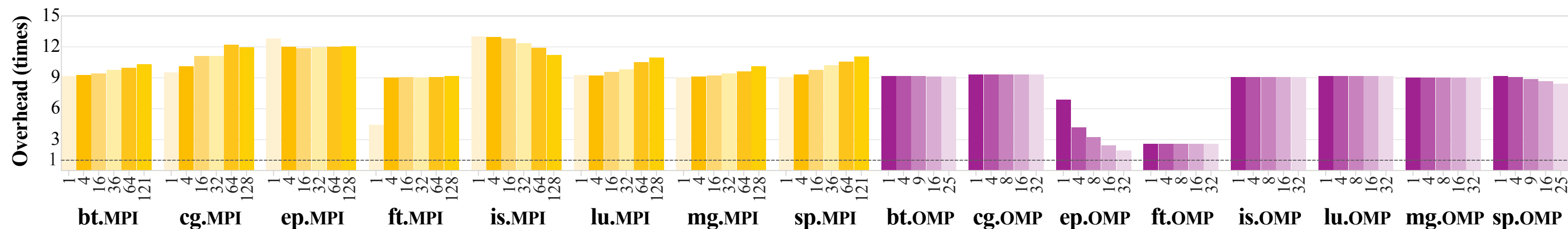


NPB Data-Centric Overhead

Runtime Overhead [**MPI 3.8x**] [**OMP 4.6x**]



Space Overhead [**MPI 11x**] [**OMP 9x**]



Real Application Overhead

Real application configuration

LAMMPS, Sweep3D

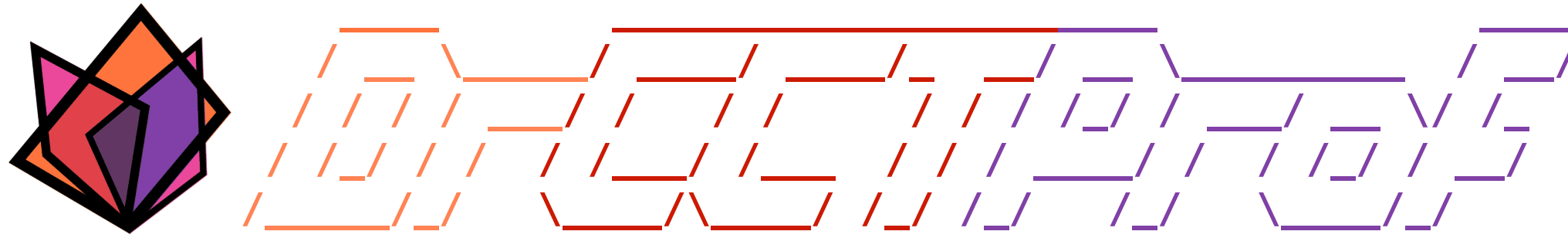
- **16 Nodes** with **512 MPI processes**

GROMACS

- **16 Nodes** with **128 MPI processes** and **4 threads** in each process

Real Applications	Original program		Code centric		Data centric	
	<i>Runtime in sec</i>	<i>Memory in KB</i>	<i>Time Overhead</i>	<i>Memory Overhead</i>	<i>Time Overhead</i>	<i>Memory Overhead</i>
LAMMPS	27.05	50968576	4.98×	2.44×	5.22×	11.06×
Sweep3D	34.80	99567728	7.56×	1.73×	7.63×	11.28×
GROMACS	35.71	4686336	5.95×	4.56×	6.17×	11.91×

The runtime and memory overheads of real applications.



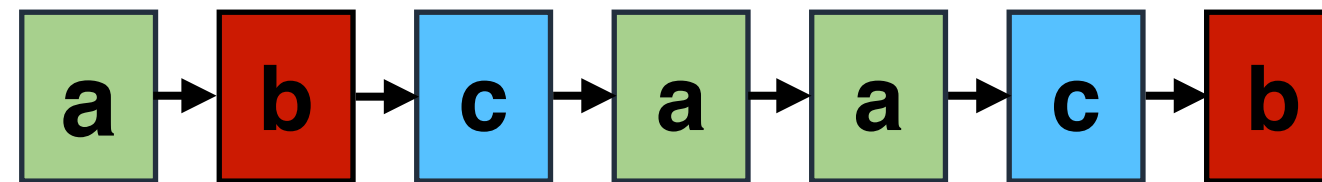
- Ubiquitous call path collection
- Attributing costs to data objects
- Merge attributions
- Evaluation
- **Case study**
- Conclusions

A DrCCTProf Client Tool: ARMREUSE

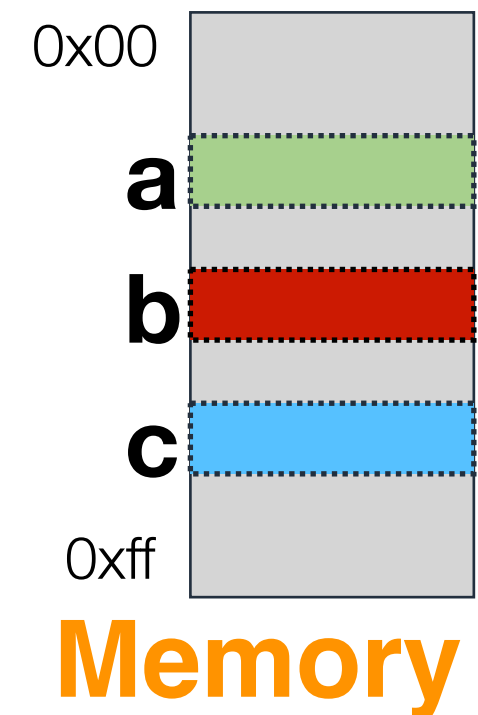
ARMREUSE

A DrCCTProf Client Tool: ARMREUSE

ARMREUSE

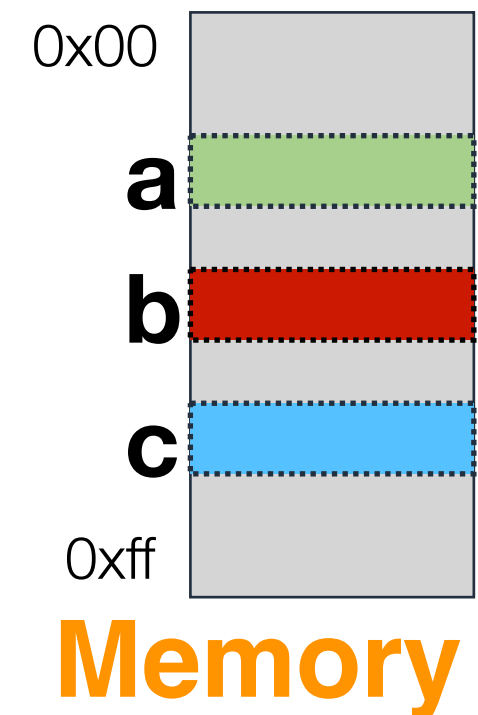
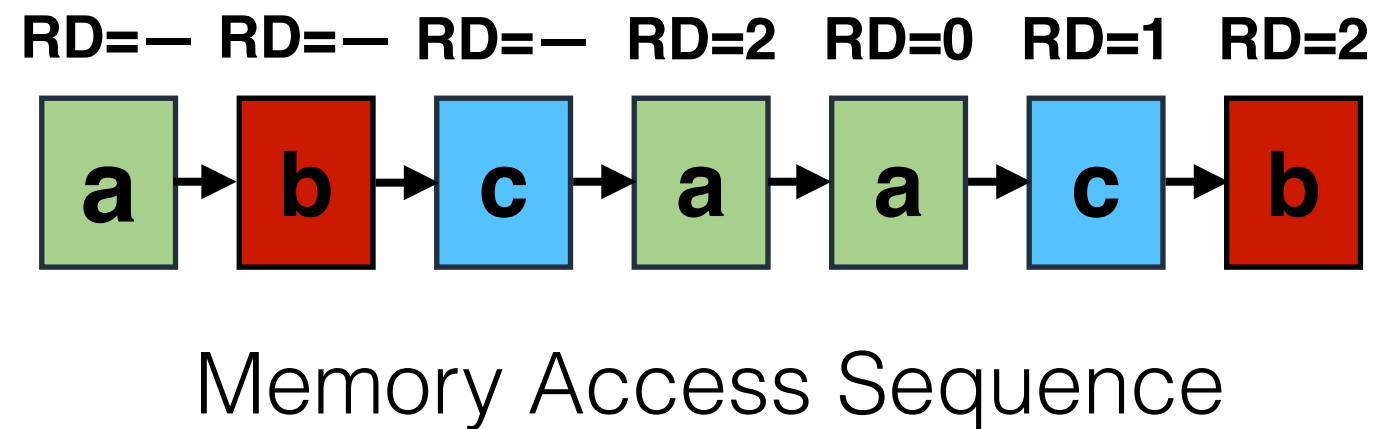


Memory Access Sequence



A DrCCTProf Client Tool: ARMREUSE

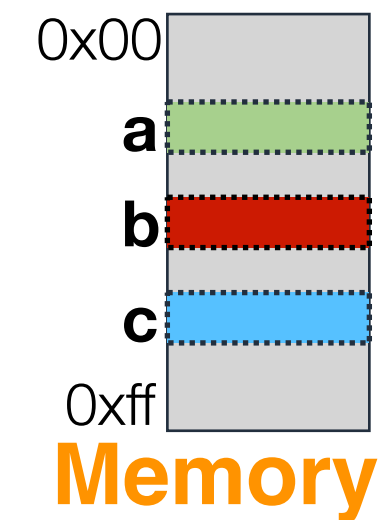
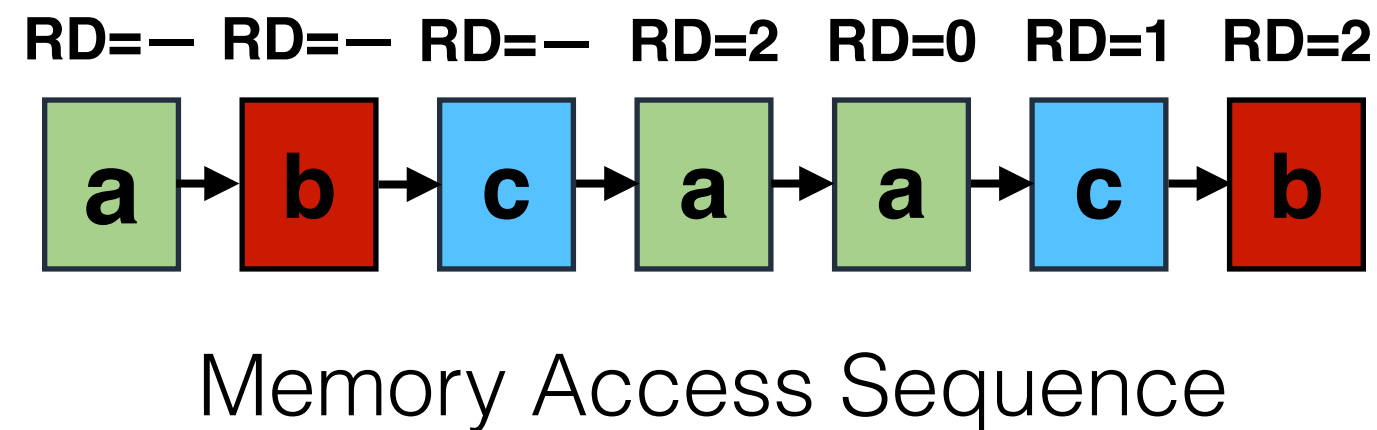
ARMREUSE



A DrCCTProf Client Tool: ARMREUSE

ARMREUSE

- Identifying temporal/spatial memory reuse pairs and computing reuse distances
- Obtaining rich insights: full call paths for use and reuse, data-centric attribution
- Providing intuitive analysis: GUI
- Implementing in an easy way: ~**500 lines** of code
- Guiding code optimization: LULESH, Sweep3D, E3SM ASME



LULESH: An LLNL Proxy Application

luleshOMP-0611.cc

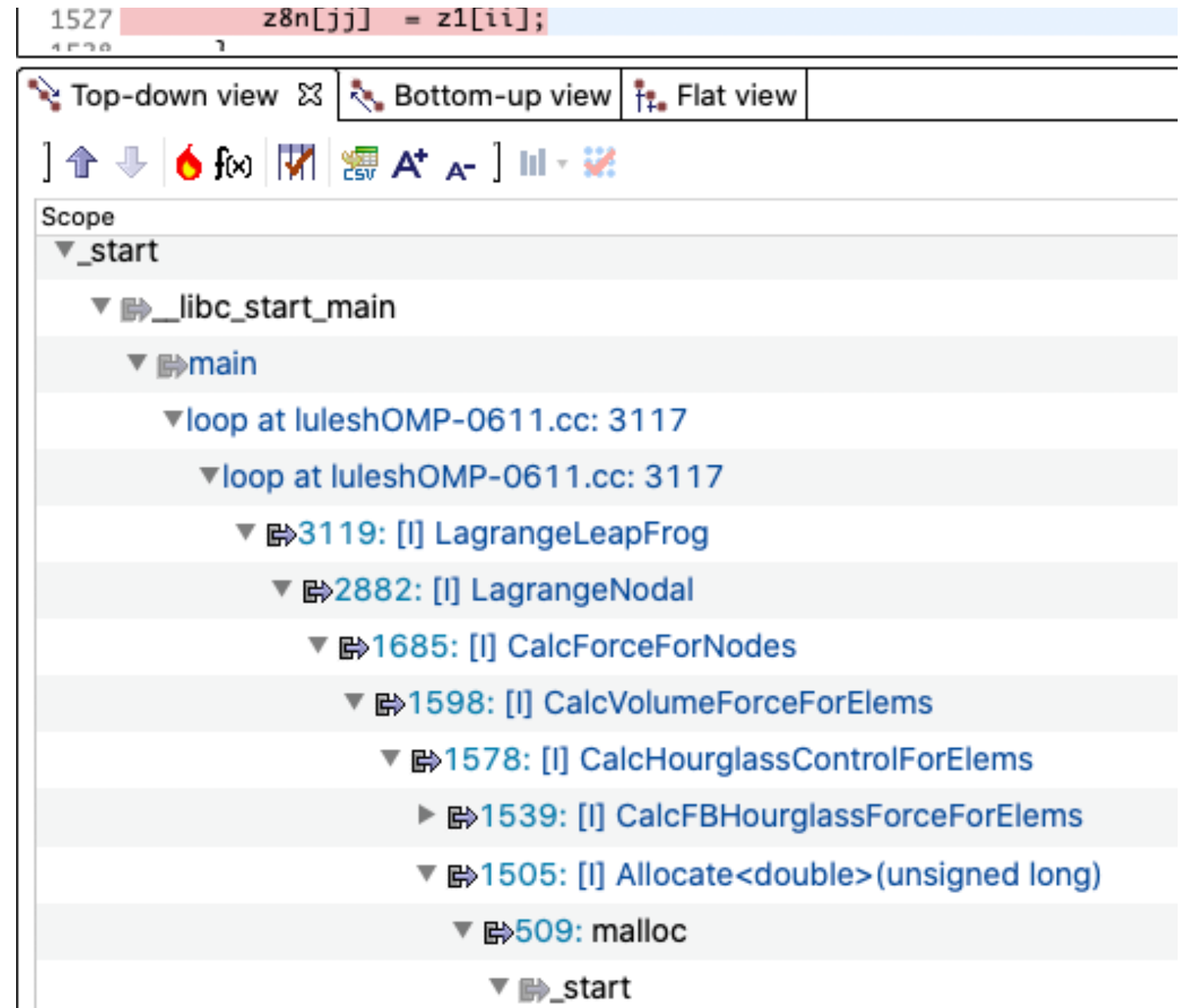
```
1583 Real_t *x8n = Allocate<Real_t>(numElem8);
1584 Real_t *y8n = Allocate<Real_t>(numElem8);
1585 Real_t *z8n = Allocate<Real_t>(numElem8);
1586
1587 /* start loop over elements */
1588 #pragma omp parallel for firstprivate(numElem)
1589 for (Index_t ii=0; ii<numElem; ++ii){
1590     Real_t x1[8], y1[8], z1[8];
1591     Real_t pfx[8], pfy[8], pfz[8];
1592
1593     Index_t* elemToNode = domain.nodeList(ii);
1594     CollectDomainNodesToElemNodes(elemToNode, x1, y1, z1);
1595
1596     CalcElemVolumeDerivative(pfx, pfy, pfz, x1, y1, z1);
1597
1598     /* load into temporary storage for FB Hour Glass control */
1599     for(Index_t ii=0; ii<8; ++ii){
1600         Index_t jj=8*ii;
1601
1602         dwdx[jj] = pfx[ii];
1603         dwdy[jj] = pfy[ii];
1604         dwdz[jj] = pfz[ii];
1605         x8n[jj] = x1[ii];
1606         y8n[jj] = y1[ii];
1607         z8n[jj] = z1[ii];
1608     }
1609 }
```

Top-down view Bottom-up view Flat view

Scope

	SUM	COUNT	[0]	[1]	[2]
▼ _start	8.46e+06	100	%		
▼ _libc_start_main	8.46e+06	100	%		
▼ main	8.46e+06	100	%		
▼ loop at luleshOMP-0611.cc: 3117	4.24e+06	50.1	%		
▼ loop at luleshOMP-0611.cc: 3117	4.24e+06	50.1	%		
▼ 3119: [] LagrangeLeapFrog	4.24e+06	50.1	%		
▼ 2882: [] LagrangeNodal	4.24e+06	50.1	%		
▼ 1685: [] CalcForceForNodes	4.24e+06	50.1	%		
▼ 1598: [] CalcVolumeForceForElems	4.24e+06	50.1	%		
▼ 1578: [] CalcHourglassControlForElems	3.96e+06	46.7	%		
▶ 1539: [] CalcFBHourglassForceForElems	1.10e+06	13.0	%		
▼ 1505: [] Allocate<double>(unsigned long)	4.92e+05	5.8	%		
▼ 509: malloc	4.92e+05	5.8	%		
▼ _start	4.92e+05	5.8	%		
▼ _libc_start_main	4.92e+05	5.8	%		
▼ main	4.92e+05	5.8	%		
▼ loop at luleshOMP-0611.cc: 3117	4.92e+05	5.8	%		
▼ loop at luleshOMP-0611.cc: 3117	4.92e+05	5.8	%		
▼ 3119: [] LagrangeLeapFrog	4.92e+05	5.8	%		
▼ 2882: [] LagrangeNodal	4.92e+05	5.8	%		
▼ 1685: [] CalcForceForNodes	4.92e+05	5.8	%		
▼ 1598: [] CalcVolumeForceForElems	4.92e+05	5.8	%		
▼ 1578: [] CalcHourglassControlForElems	4.92e+05	5.8	%		
▼ 1508: GOMP_parallel	3.03e+05	3.6	%		
▼ 168: CalcHourglassControlForElems(double*, double) [clone _ov	3.03e+05	3.6	%		
▼ loop at luleshOMP-0611.cc: 1533	3.03e+05	3.6	%		
▶ 1527: _start	3.79e+04	0.4	%		
▼ 1527: _start	3.79e+04	0.4	%		
▼ _libc_start_main	3.79e+04	0.4	%		
▼ main	3.79e+04	0.4	%		
▼ loop at luleshOMP-0611.cc: 3117	3.79e+04	0.4	%		
▼ loop at luleshOMP-0611.cc: 3117	3.79e+04	0.4	%		
▼ 3119: [] LagrangeLeapFrog	3.79e+04	0.4	%		
▼ 2882: [] LagrangeNodal	3.79e+04	0.4	%		
▼ 1685: [] CalcForceForNodes	3.79e+04	0.4	%		
▼ 1598: [] CalcVolumeForceForElems	3.79e+04	0.4	%		
▼ 1578: [] CalcHourglassControlForElems	3.79e+04	0.4	%		
▼ 1539: [] CalcFBHourglassForceForElems	3.79e+04	0.4	%		
▼ 1283: GOMP_parallel	3.79e+04	0.4	%		
▼ 168: CalcFBHourglassForceForElems	3.79e+04	0.4	%		
▼ loop at luleshOMP-0611.cc: 1314	3.79e+04	0.4	%		
luleshOMP-0611.cc: 1314	3.79e+04	0.4	%		

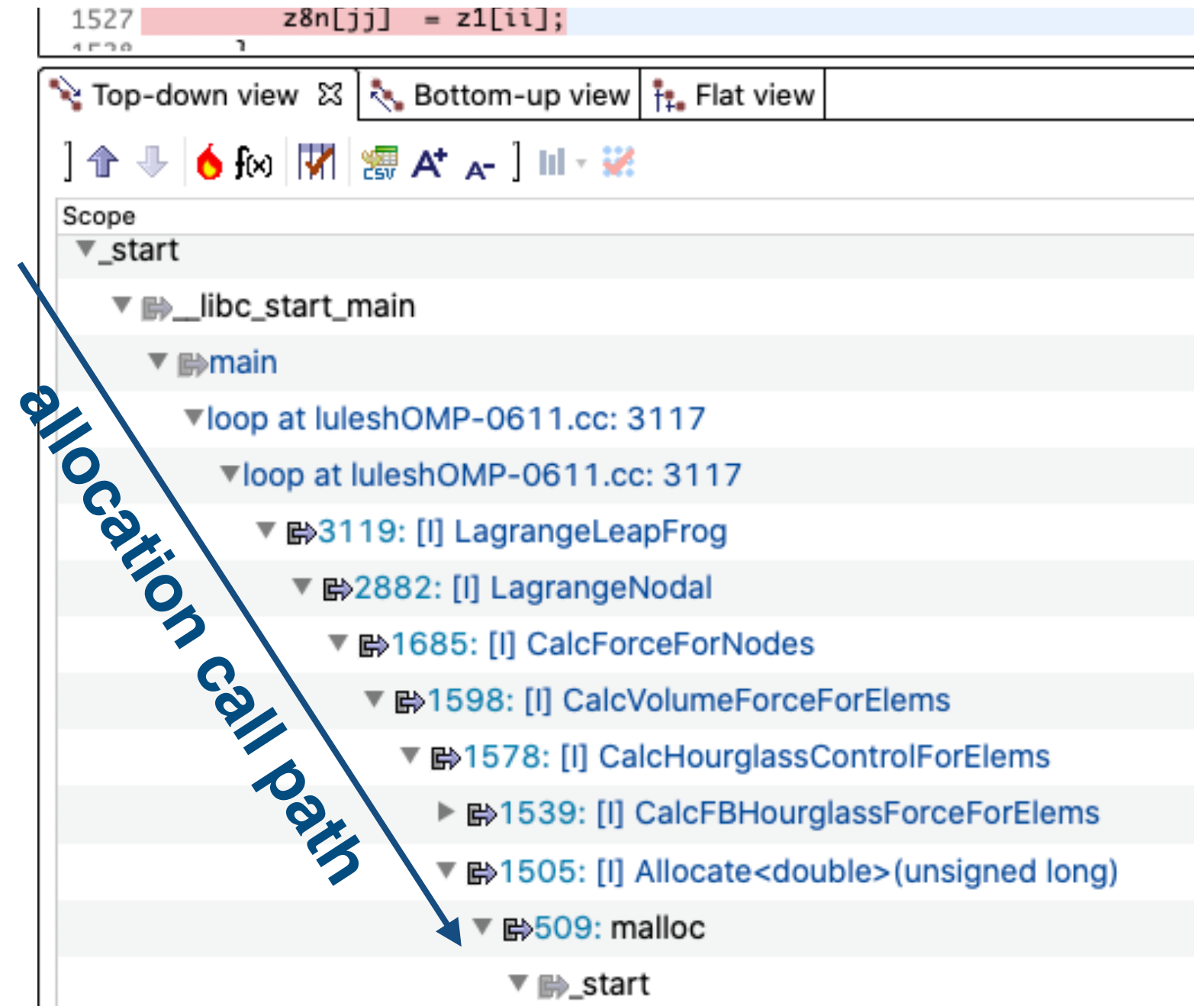
LULESH: An LLNL Proxy Application



LULESH: An LLNL Proxy Application

Allocation Call Path

The top reuse pairs on ***z8n*** accounts for 3.6% of total temporal reuse.



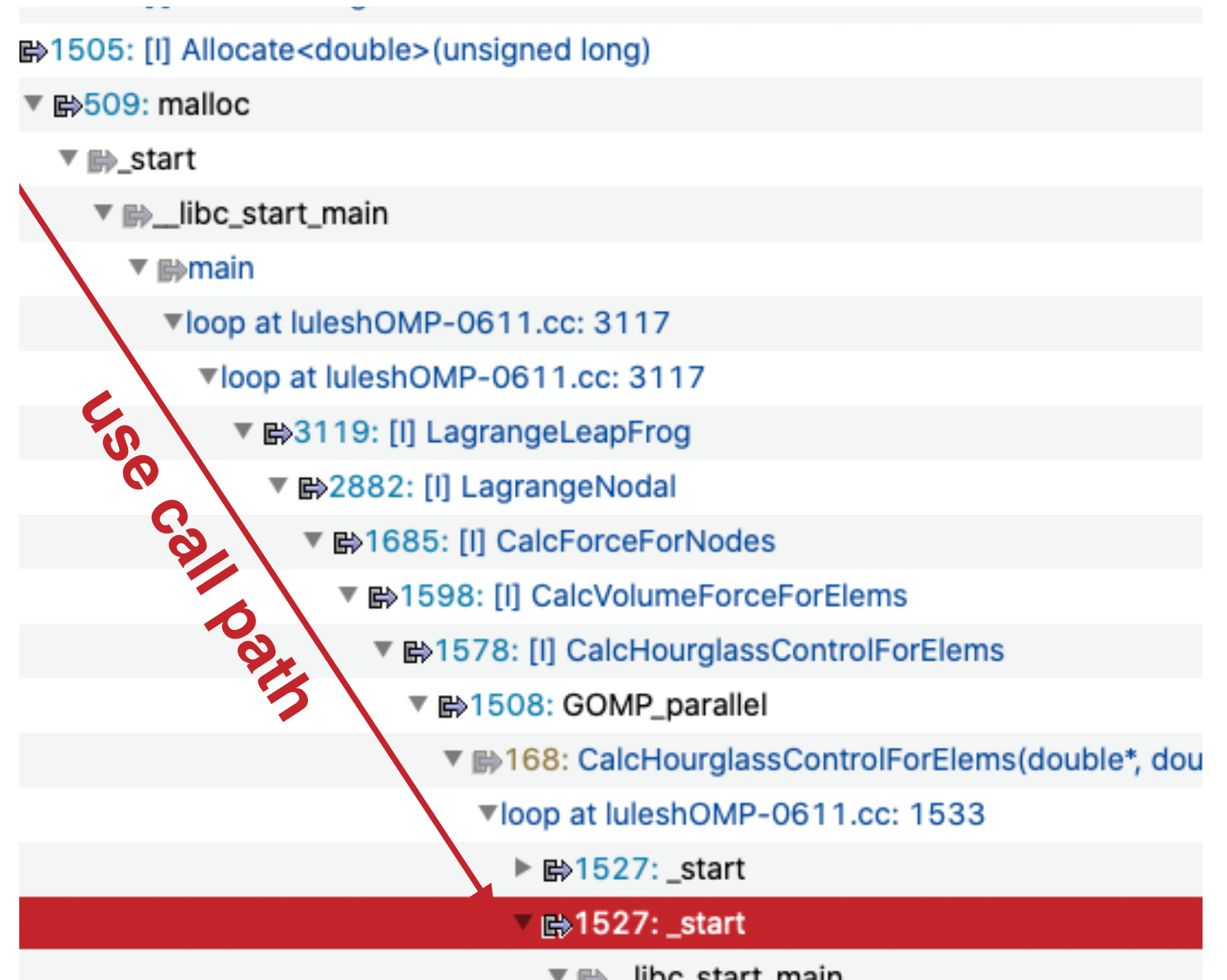
LULESH: An LLNL Proxy Application

Allocation Call Path

The top reuse pairs on **z8n** accounts for 3.6% of total temporal reuse.

Use Call Path

The use is the memory access at line 1527 (in the loop at line 1509)



LULESH: An LLNL Proxy Application

Allocation Call Path

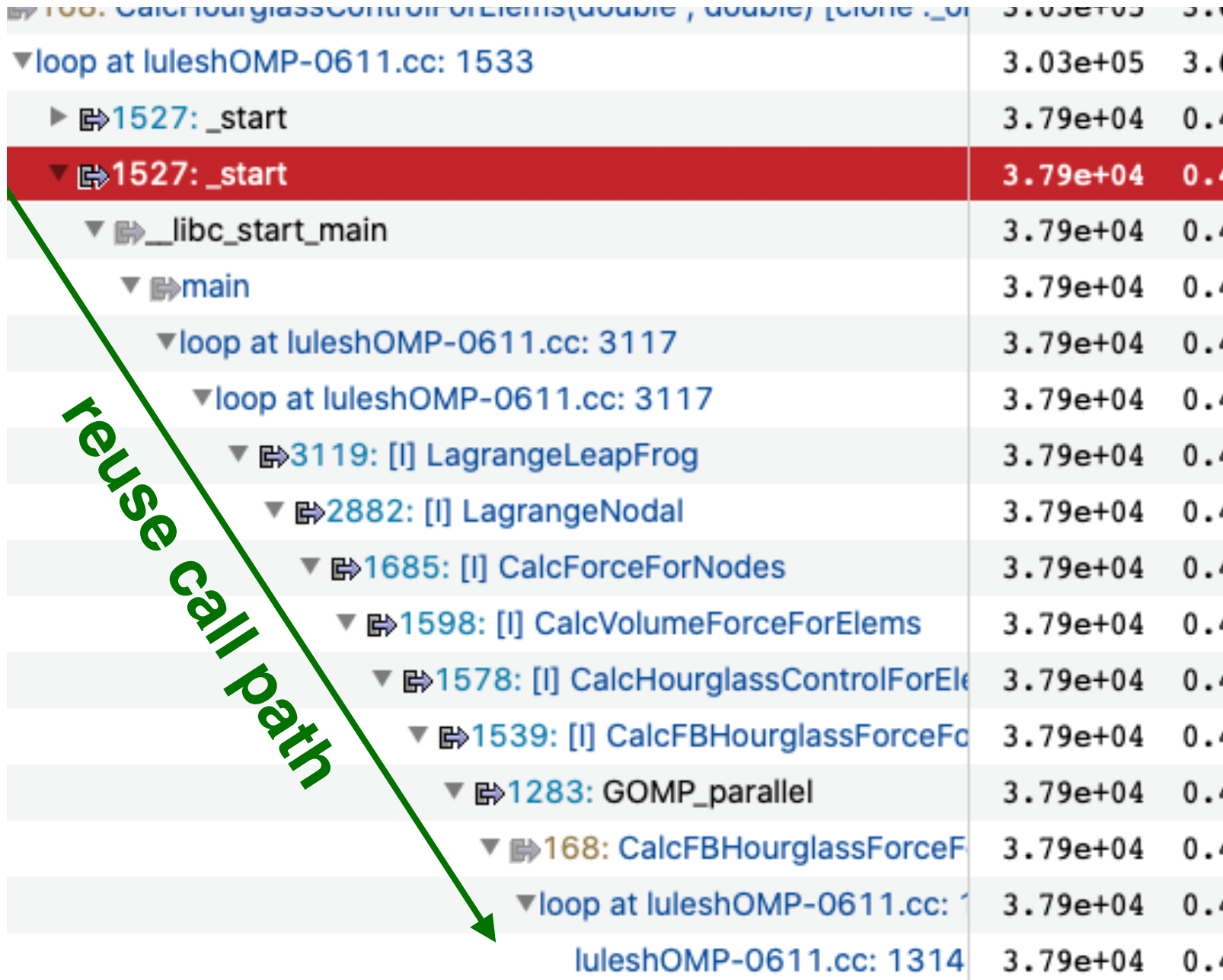
The top reuse pairs on **z8n** accounts for 3.6% of total temporal reuse.

Use Call Path

The use is the memory access at line 1527 (in the loop at line 1509)

Reuse Call Path

The reuse is the memory access at line 1314 (in the loop at line 1284)



loop at luleshOMP-0611.cc: 1533	3.03e+05	3.0
1527: _start	3.79e+04	0.4
1527: _start	3.79e+04	0.4
_libc_start_main	3.79e+04	0.4
main	3.79e+04	0.4
loop at luleshOMP-0611.cc: 3117	3.79e+04	0.4
loop at luleshOMP-0611.cc: 3117	3.79e+04	0.4
3119: [I] LagrangeLeapFrog	3.79e+04	0.4
2882: [I] LagrangeNodal	3.79e+04	0.4
1685: [I] CalcForceForNodes	3.79e+04	0.4
1598: [I] CalcVolumeForceForElems	3.79e+04	0.4
1578: [I] CalcHourglassControlForEle	3.79e+04	0.4
1539: [I] CalcFBHourglassForceFo	3.79e+04	0.4
1283: GOMP_parallel	3.79e+04	0.4
168: CalcFBHourglassForceF	3.79e+04	0.4
loop at luleshOMP-0611.cc: 1314	3.79e+04	0.4
luleshOMP-0611.cc: 1314	3.79e+04	0.4

LULESH: An LLNL Proxy Application

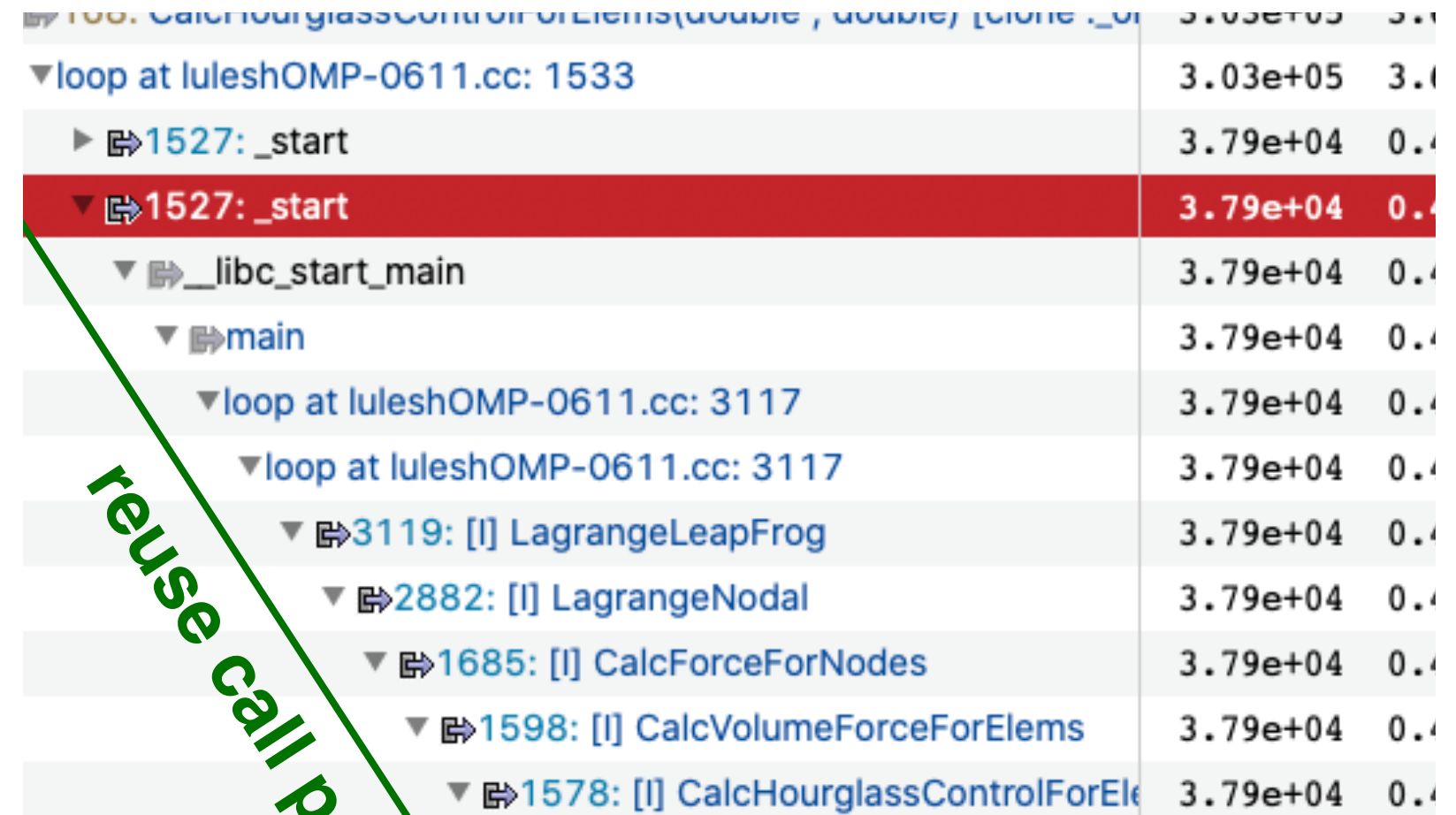
Allocation Call Path

The top reuse pairs on **z8n** accounts for 3.6% of total temporal reuse.

Use Call Path

The use is the memory access at line 1527 (in the loop at line 1509)

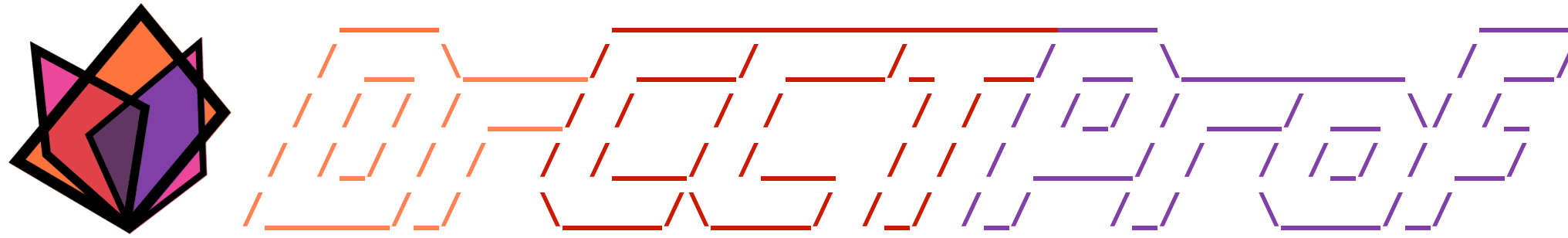
Reuse Call Path



100: CalcHourglassControlForElems(double, double) [clone .l...	3.03e+05	3.0
▼ loop at luleshOMP-0611.cc: 1533		
▶ 1527: _start	3.79e+04	0.4
▼ 1527: _start	3.79e+04	0.4
▼ _libc_start_main	3.79e+04	0.4
▼ main	3.79e+04	0.4
▼ loop at luleshOMP-0611.cc: 3117	3.79e+04	0.4
▼ loop at luleshOMP-0611.cc: 3117	3.79e+04	0.4
▼ 3119: [I] LagrangeLeapFrog	3.79e+04	0.4
▼ 2882: [I] LagrangeNodal	3.79e+04	0.4
▼ 1685: [I] CalcForceForNodes	3.79e+04	0.4
▼ 1598: [I] CalcVolumeForceForElems	3.79e+04	0.4
▼ 1578: [I] CalcHourglassControlForEle	3.79e+04	0.4

Hoist the two loops (line 1508 and 1283) into their least common ancestor in the call paths and fuse them

1.28x speedup



- Ubiquitous call path collection
- Attributing costs to data objects
- Merge attributions
- Evaluation
- Case study
- **Conclusions**

Conclusions

DrCCTProf

- A practical fine-grained call path profiler framework for ARM/x86 binaries
- Strong support for various analysis tools
- Moderate time and memory overheads
- Applicable to large-scale executions

Open source with MIT license

<https://github.com/Xuhpclab/DrCCTProf>

On-Going Work (Will Release Soon)

Supporting various binaries

Python

Go

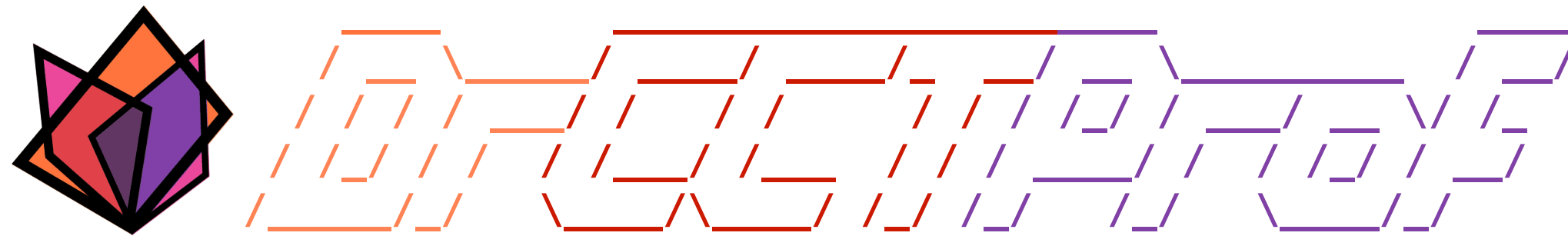
Rust

...

Supporting accelerators

X86+GPU, ARM+GPU

Q&A



<https://github.com/Xuhpclab/DrCCTProf>

Hands-on Lab

Machine access

```
ssh drcctprof1@rocco.cs.wm.edu -p 11111
```

```
Password: *drcctproftest1#
```

Create your own directory under this account

```
mkdir XX
```

```
cd XX
```

Download and build DrCCTProf

```
git clone --recurse https://github.com/Xuhpclab/drcctprof\_tutorial.git
```

```
cd drcctprof_tutorial
```

```
./build.sh
```

Develop the client

```
src/client.cpp: 42 - 55
```